An Abstract Interpretation with the Interval Domain for C-like Programs

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Abstract

We defined an intermediate representation called G for representing programs written in imperative languages like C. We designed an abstract interpretation of G using the interval domain and proved its soundness. As a real world application, we implemented an abstract interpreter that analyzes array index ranges, and finds buffer overrun errors in real C programs. Implementation techniques used in our analyzer and its performance are presented.

1 Overview

1.1 Introduction

As the software industry matures, demands for static program analysis and verification tools are increasing. The industry understands software testing approach is neither efficient nor sufficient for quality assurance. Test runs can find some bugs at their best, but can never prove the software is free from bugs. Although ad-hoc static bug finding tools are becoming popular, they have the same problem as software testing, since they may still miss true errors. As software industry becomes more serious, people will eventually want to use conservative yet effective software verification tools.

In order to build good static analysis tools for C-like programs, we designed an abstract interpretation with the interval domain. To focus on the abstract interpretation, we designed a simple intermediate form called G. We believe programs written in languages similar to the C Programming Language[14] can be transformed into G straightforwardly. We defined the abstract interpretation of G, and proved its soundness on top of the Abstract Interpretation Framework[7]. It is a theoretical framework for static program analyzer we built, and will be a starting point for static program verifiers we plan to build.

This chapter gives a brief overview of our problem and approach, then discusses related work and directions. Chapter 2 describes the syntax and semantics of G form. Chapter 3 defines the abstract semantics of G using the integer interval domain[6], and Chapter 4 presents the proof of its soundness. Chapter 5 suggests some techniques of implementation that will improve the performance of the abstract interpreter. Chapter 6 shows a realistic application of our abstract interpretation on the buffer overrun problem.

1.2 Discussion

Related work Value Set Analysis[2] is an approach very similar to ours. The major difference is that they are targeted at analyzing executables to reject malicious plugins and
mobile codes or analyze worms and viruses, while we are analyzing source programs to help the programmers fix bugs in them. Analyzing executables clearly has the advantage that bugs introduced by compilers can also be detected. However, without source code information, it is very hard to provide useful debug information to program developers, and the analysis can only decide the correctness of the program.

Report of ASTRÉE[3, 4] mentions the idea of localizing operators in Section 5.3. No detail is provided but only a brief, incomplete explanation is there why localizing operators on their abstract environment is possible. We provide detailed arguments on what conditions make it possible to localize operators on abstract memory and how one can do it almost independently of other parts of the analyzer.

Huge body of work on buffer over-run errors are mentioned in Chapter 6.

Advantages of abstract interpretation We believe our strong point is the fact that abstract semantics resembles concrete semantics. Because of that, programmers can intuitively understand its results, and analysis designers can improve its accuracy in a natural way. Results of set-based analysis[12] or other constraint based analyses, especially for C-like programs, are hard to understand for program developers. Interpreting or summarizing those results for the user requires lots of effort. The abstraction is not explicitly handled so hard to control systematically. Moreover, there is no systematic guide for designing and establishing soundness of them as well as analyses based on type or effect systems. It is a huge burden for the designer that he must find the right way out of the full darkness all by himself. Various program analysis techniques based on data flow analyses framework are usually composed of several separate analyses, e.g. control flow, pointer and interval analyses. The separation of analyses makes improving the accuracy hard when the information analyzed at later steps are required by the early steps. Our approach does not suffer from such problem since it computes every information as a single fixpoint.

Future work The syntax of $G$ form defined in this report is not expressive enough to represent all C programs. Neither explicit type castings nor implicit ones of C can be expressed in $G$. We will handle it appropriately by introducing memory and address operators with type size information into $G$.

The semantics of $G$ needs to explicitly handle error conditions. Currently, we do not define any semantics for erroneous executions. Therefore, the abstract semantics after such points is $\bot$. However, since $\bot \subseteq m$ for all $m$, it becomes ambiguous whether an abstract memory $m$ also means erroneous state or not. Since we are interested in errors in programs, distinguishing these two are very important. A similar problem to this is in treatment of free variables. We define no semantics for use of free variables. Practically, there are always free variables in real programs, i.e. there are parts whose source code is not available. To make the analysis more robust, we must give explicit concrete semantics for free variables and corresponding sound abstract semantics.

Several improvements to the abstract semantics of $G$ will significantly increase the analysis accuracy. First, our abstract memory update must be improved to a sound, strong update. Although our analysis is flow sensitive, i.e. computes different abstract memory for each point, because of weak update, possible values of previous points get accumulated to later ones. We need strong update to fully benefit from our flow sensitivity. Second, we need to make it possible to distinguish values of array elements. Currently, we use a single abstract memory cell for array elements. This limits us from accurately analyzing programs with arrays having fixed contents. A systematic method for partitioning the indexes of elements must be carried out. Third, we need to isolate local variables of procedures. Since join or widening, narrowing on memory are unaware of reachability, such operation in one procedure will also be applied to cells of local variables of other procedures which are definitely unreachable from that point. Computing the reachability for every join, widening must be an expensive choice. Therefore, we can get some help from syntax, and simply isolate only the local variables to the owning procedure.

We are designing a systematic method for increasing the analysis accuracy to reduce the number of false alarms. Using the trace partitioning technique[17], we can control various
path and context sensitivities of the analysis. We will be adopting it into our analysis by extending the abstractions $\alpha_{ctrl}, \alpha_{data}$ and domain of abstract addresses $\hat{Addr}$.

To become a true verification tool, the analyzer must provide a way to eliminate false alarms. Although false alarms are inherently inevitable for static analysis, most of the verification process is a job too routined for humans to perform. Using the systematic way of increasing the analysis accuracy, we will build a persistent verification framework. We believe it will automate most of the analysis and verification process.

We are working on other applications, e.g. detection of memory leaks and uses of dangling pointers. They are also a major problem for C-like programs as well as buffer overruns. By slightly extending our instrumentation on the $\mathcal{G}$ form, we can check the result of the abstract interpretation to detect those errors.

2 G Programs

2.1 Representation

A $\mathcal{G}$ program $P$ is a set of $\mathcal{G}$ procedures.

$$P \in \texttt{Pgm} = \wp(\texttt{Proc})$$

A $\mathcal{G}$ procedure has its name, formal parameters\(^1\), and a CFG which represents its body.

$$\texttt{Proc} = \texttt{ProcId} \times \texttt{Var} \times \texttt{CFG}$$

A control flow graph\([1, \S 9.4] \in \texttt{CFG}

- is a directed graph whose nodes are basic blocks $\in \texttt{Block}$.
- has a unique $\texttt{ENTRY}_p$ node where every flow begins.
- has a unique $\texttt{EXIT}_p$ node where all flows began from $\texttt{ENTRY}_p$ end.
- has two types of edges: flow edges and resume edges. Resume edges remember the node to resume after a procedure call from a call node.

$$\texttt{CFG} = \texttt{Flows} \times \texttt{Resumes}$$

$$\texttt{Flows} = \texttt{Edges}$$

$$\texttt{Resumes} = \texttt{Edges}$$

$$\texttt{Edges} = \wp(\texttt{Block} \times \texttt{Block})$$

There are three types of identifiers: local, global and field names. Name of procedure is also a global name.

- local name $x \in \texttt{Var}$
- global name $X \in \texttt{GVar}$
- procedure name $p \in \texttt{ProcId} \subseteq \texttt{GVar}$
- field name $f \in \texttt{FieldId}$
- allocation site $t \in \texttt{AllocSite}$

Basic block $b \in \texttt{Block}$ constituting a control flow graph is defined by the following syntax:

| block $b \in \texttt{Block}$
| command $c \in \texttt{Cmd}$
| expression $e \in \texttt{Expr}$
| relation $r \in \texttt{Rel}$
| shape $s \in \texttt{Shape}$

\(^1\)For simplicity, only one parameter is considered here.
For simplicity, let us assume every basic block $b \in \text{Block}_P$ of a program $P$ is always distinguishable. Under this assumption, keeping separate sets of edges for each procedure is not very useful. So from here on, let us only consider the two sets of edges of the program, i.e. $\text{Flows}_p$ and $\text{Resumes}_p$, and define procedures as $\text{Proc} = \text{ProcId} \times \text{Var}$. Nevertheless, let us use $b^p$ to denote that $b$ belongs to procedure $p$.

2.2 Semantics

2.2.1 Domain

Here are the domains used in our semantics definition:

\[
\begin{align*}
\tau \in \text{Trace} &= \text{State}^* \\
\sigma \in \text{State} &= \text{Block} \times \text{Memory} \times \text{Dump} \\
m \in \text{Memory} &= \text{Map} \times \text{Counter} \times \text{Alloc} \\
M \in \text{Map} &= \text{Addr} \overset{\text{in}}{\rightarrow} \text{Val} \\
v \in \text{Val} &= \text{Z} + \text{Addr} \\
a \in \text{Addr} &= \text{GVar} + \text{Dump} \times \text{Var} + \\
&\quad \text{Region} \times \text{Index} \times \text{Region} \times \text{FieldId} \\
r \in \text{Region} &= \text{Counter} \times \text{AllocSite} \\
c \in \text{Counter} &= \text{N} \\
I \in \text{Alloc} &= \text{Region} \overset{\text{in}}{\rightarrow} (\text{Size} + \text{FieldId}^*) \\
\text{Size} &= \text{N} \\
\text{Index} &= \text{Z} \\
d \in \text{Dump} &= (\text{ProcId} \times \text{Block} \times \text{Addr}^*)
\end{align*}
\]

2.2.2 Traces

Let us use $P \in \text{Pgm}$ to refer to the $G$ program we are considering throughout this section. Its collecting semantics $[P] \Sigma_0$ is the prefix closure of traces whose states are all connected by the transition $\rightarrow$ and begins from an initial state in $\Sigma_0$.

\[
[P] : \forall (\text{State}) \rightarrow \forall (\text{Trace})
\]

\[
[P] \Sigma_0 = \{ (\sigma_1, \cdots, \sigma_n) : \text{Trace} \mid \sigma_1 \in \Sigma_0, \sigma_i \rightarrow \sigma_{i+1} \}
\]

$[P]$ can also be expressed as the least fixpoint of a semantic transfer function $F$ greater than or equal to $\Sigma_0$.

\[
[P] \Sigma_0 = \text{lfp}_{\Sigma_0}(F P)
\]

where

\[
F : \text{Pgm} \rightarrow \forall (\text{Trace}) \rightarrow \forall (\text{Trace})
\]

\[
F P T = T \cup \{ (\sigma_1, \cdots, \sigma_n, \sigma_{n+1}) \mid (\sigma_1, \cdots, \sigma_n) \in T, \sigma_n \rightarrow \sigma_{n+1} \}
\]

Usually, if main is the entry procedure, and $\text{Map}'_0$ is the set of value mappings for free variables, then the set of initial states $\Sigma_0$ is determined as the following:

\[
\Sigma_0 = \{(\text{ENTRY}_{\text{main}}, (M_0, 0, \emptyset), e) : \text{State} \mid M_0 = M[p \mapsto p \mid (p, x) \in P], M \in \text{Map}'_0 \}
\]

2.2.3 Transition

The transition $\rightarrow$ is a binary relation on $\text{State}$. $R$ and $V$, $A$ defines the semantics of commands and expressions respectively.

\[
(b, m, d) \rightarrow (b', m', d')
\]

if and only if
when \( b = \text{CALL}(e, e_0, e_1) \),

\[
\begin{align*}
p' & : \text{Procld} & b' & = \text{ENTRY}_{p'}; \\
p' & = \mathcal{A} d m e_0 & d' & = (p', b_1, \mathcal{A} d m e) \mapsto d \\
(p', x) & \in P & m' & = (M[d', x] \mapsto 'V d m e_1, c, l) \\
(b, b') & \in \text{Resumes}_{p} & \text{where } (M, c, l) = m
\end{align*}
\]

• when \( b = \text{ENTRY}_{p'}, \)

\[
\text{(ENTRY}_{p'}, b') \in \text{Flows}_{p'} \\
(m', d') = (m, d)
\]

• when \( b = \text{EXIT}_{p'}, \)

\[
(p', b', \_ \mapsto d') = d \\
m' = m
\]

• when \( b = (c_1, \ldots, c_n), \)

\[
(b, b') \in \text{Flows}_{p'} \\
m' = (R d c_1; \cdots ; R d c_n) m \\
d' = d
\]

2.2.4 Reaction

Each command changes the memory part of the state. Some commands only change the value mapping, and some also modifies the allocation information.

\[
\mathcal{R} : \text{Dump} \rightarrow \text{Cmd} \rightarrow \text{Memory} \rightarrow \text{Memory}
\]

\[
\begin{align*}
\mathcal{R} d \text{SET}(c_x, e) (M, c, l) & = (M[d M e_1 \mapsto 'V d M e], c, l) \\
\mathcal{R} d \text{ESCAPE}(e) (M, c, l) & = (M[a \mapsto 'V d M e], c, l) \\
\text{where } (d, a) & \mapsto d \\
\mathcal{R} d \text{ASSUME}(r) (M, c, l) & = (M, c, l) \text{ if } d, (M, c, l) \models r \\
\mathcal{R} d \text{ALLOC}(c_x, [e])_1 (M, c, l) & = (M[d M e_1 \mapsto (r, 0)], c + 1, l[r \mapsto 'V d M e]) \\
\text{where } r & = (c, l) \\
\mathcal{R} d \text{ALLOC}(e_0, [\bar{f}])_1 (M, c, l) & = (M[d M e_1 \mapsto (r, f)], c + 1, l[r \mapsto 'f]) \\
\text{where } r & = (c, l) \\
\mathcal{R} d \text{FREE}(e) (M, c, l) & = (M - [a], c, l - [a]) \\
\text{where } a & = \mathcal{A} d M e
\end{align*}
\]

2.2.5 Value

Expression defines a value \( \text{Val} \) under its context, dump \( \text{Dump} \) and value mapping \( \text{Map} \).

\[
\mathcal{V} : \text{Dump} \rightarrow \text{Map} \rightarrow \text{Expr} \rightarrow \text{Val}
\]

\[
\begin{align*}
\mathcal{V} d M n & = n \\
\mathcal{V} d M x & = X \\
\mathcal{V} d M (d, x) & = (d, x) \\
\mathcal{V} d M e_1 \ast e_2 & = 'V d M e_1 \ast 'V d M e_2 \\
\mathcal{V} d M e. f & = (r, f) \text{ where } (r, \_ ) = \mathcal{A} d M e \\
\mathcal{V} d M \bar{e} & = M(\mathcal{A} d M e)
\end{align*}
\]

For convenience, let us overload \( \mathcal{V} \) and also use it on \( \text{Memory} \).

\[
\mathcal{V} : \text{Dump} \rightarrow \text{Memory} \rightarrow \text{Expr} \rightarrow \text{Val}
\]

\[
\mathcal{V} d (M, \_ \_ ) e = 'V d M e
\]
2.2.6 Address

There are restrictions to some values so that they are not allowed to be used as addresses.

\[ \mathcal{A} : \text{Dump} \rightarrow \text{Map} \rightarrow \text{Expr} \rightarrow \text{Addr} \]

\[ \mathcal{A} \text{d M e} = V \text{d M e} \]

2.2.7 Consistency

\[ d, m \models r \] reads “dump \( d \) and memory \( m \) are consistent with assertion \( r \).”

\[
\begin{align*}
\forall d \text{ m e}_1 = V d \text{ m e}_2 & \quad \forall d \text{ m e}_1 \neq V d \text{ m e}_2 \\
\forall d, m \models e_1 = e_2 & \quad \forall d, m \models e_1 \neq e_2 \\
\forall d, m \models e_1 < e_2 & \quad \forall d, m \models e_1 > e_2 \\
\forall d, m \models e_1 < e_2 & \quad \forall d, m \models e_1 = e_2 \\
\forall d, m \models e_1 > e_2 & \quad \forall d, m \models e_1 = e_2 \\
\forall d, m \models e_1 < e_2 & \quad \forall d, m \models e_1 > e_2 \\
\forall d, m \models e_1 \leq e_2 & \quad \forall d, m \models e_1 \geq e_2 \\
\forall d, m \models e_1 \geq e_2 & \quad \forall d, m \models e_1 \leq e_2 \\
\forall d, m \models e_1 \leq e_2 & \quad \forall d, m \models e_1 \geq e_2
\end{align*}
\]

Order on \( \mathbb{Z} \) are defined as usual, and order between two indexed addresses in \( \text{Region} \times \text{Index} \) are defined as the order on \( \text{Index} \) if their \( \text{Region} \) coincides.

2.2.8 Operations

Let us consider four binary operations, addition(\( + \)), subtraction(\( - \)), multiplication(\( \ast \)), and division(\( / \)) denoted as \( \star \) in the previous definitions.

Operations on \( \mathbb{Z} \) are defined as usual.

\[
\begin{align*}
+ & : \mathbb{Z} \times \mathbb{Z} \rightarrow \mathbb{Z} \\
- & : \mathbb{Z} \times (\text{Region} \times \text{Index}) \rightarrow (\text{Region} \times \text{Index}) \\
\ast & : \mathbb{Z} \times \mathbb{Z} \rightarrow \mathbb{Z} \\
/ & : (\text{Region} \times \text{Index}) \times (\text{Region} \times \text{Index}) \rightarrow \mathbb{Z}
\end{align*}
\]

Addition can shift indexed addresses,

\[
\begin{align*}
(r, i) + n & = (r, i + n) \\
(r, i) \ast n & = (r, n \ast i)
\end{align*}
\]

and subtraction can compute the distance between two.

\[
\begin{align*}
(r, i) - (r, j) & = i - j
\end{align*}
\]
3 G Program Analysis

3.1 Abstraction

3.1.1 Abstract domain

\[ \hat{S} \in \text{Summary} = \hat{\text{Graph}} \times \hat{\text{Table}} \times \hat{\text{Dump}} \]
\[ \hat{G} \in \hat{\text{Graph}} = \hat{\text{Edges}} \]
\[ \hat{T} \in \hat{\text{Table}} = \hat{\text{Block}} \mapsto \hat{\text{Memory}} \]
\[ \hat{m} \in \hat{\text{Memory}} = \hat{\text{Map}} \times \hat{\text{Alloc}} \]
\[ \hat{M} \in \hat{\text{Map}} = \hat{\text{Addr}} \mapsto \hat{\text{Value}} \]
\[ \hat{V} \in \hat{\text{Value}} = \varphi(\text{Val}) \]
\[ \hat{A} \in \hat{\text{Address}} = \varphi(\text{Addr}) \]
\[ \hat{a} \in \hat{\text{Addr}} = \hat{\text{GVar}} + \text{ProcId} \times \text{Var} + \hat{\text{Region}} \times \hat{\text{Index}} + \hat{\text{Region}} \times \hat{\text{FieldId}} \]
\[ \hat{I} \in \hat{\text{Alloc}} = \hat{\text{Region}} \mapsto (\hat{\text{Size}} \times \wp(\hat{\text{FieldId}})) \]
\[ \hat{r} \in \hat{\text{Region}} = \hat{\text{AllocSite}} \]
\[ \hat{i} \in \hat{\text{Index}} = \hat{\mathbb{Z}} \]
\[ \hat{D} \in \hat{\text{Dump}} = \text{ProcId} \mapsto \hat{\text{Address}} \]

Order defined on each abstract domain respects the subset order of the corresponding concrete domain. Order on \( \hat{\text{Alloc}} \) are pointwisely defined. Intervals in \( \hat{\mathbb{Z}} \) have exactly the same order to the subset order they mean. \( \hat{\text{Addr}} \) and \( \hat{\text{Val}} \) have order naturally derived from \( \hat{\mathbb{Z}} \), and set of them in \( \hat{\text{Address}} \) and \( \hat{\text{Value}} \) are ordered if and only if all elements they contain are ordered.

3.1.2 Galois connections

We define the concretization part of each Galois connection. The abstractions are uniquely defined as following thanks to the property of Galois connection[8, theorem 5.3.0.5].

\[ \varphi(X) \xrightarrow{\gamma} \hat{X} \]
\[ \alpha X' = \{ \hat{x} \in \hat{X} | X' \subseteq \gamma \hat{x} \} \]

3.1.3 Trace abstraction

Set of traces a program \( P \) generates is summarized by three abstract components: \( \hat{\text{Graph}} \), \( \hat{\text{Table}} \), and \( \hat{\text{Dump}} \).

\[ \varphi(\text{Trace}) \xrightarrow{\gamma} \hat{\text{Graph}} \times \hat{\text{Table}} \times \hat{\text{Dump}} = \text{Summary} \]
\[ \gamma (\hat{G}, \hat{T}, \hat{D}) = \gamma_{\text{ctrl}} \hat{G} \cap \gamma_{\text{data}} \hat{T} \cap \gamma'_{\text{dump}} \hat{D} \]

Control flow abstraction

\[ \varphi(\text{Trace}) \xrightarrow{\gamma_{\text{ctrl}}} \hat{\text{Graph}} \]
\[ \gamma_{\text{ctrl}} \hat{G} = \{(b_1, \ldots, b_n) \in \text{Trace} | \forall i \in [1, \ldots, n - 1] : (b_i, b_{i+1}) \in \hat{G}\} \]

Data state abstraction

\[ \varphi(\text{Trace}) \xrightarrow{\gamma_{\text{data}}} \hat{\text{Table}} \]
\[ \gamma_{\text{data}} \hat{T} = \gamma_{\text{State}} \{ (b, m, \ldots) \in \text{State} | m \in \gamma_{\text{Memory}} (\hat{T}(b)) \} \]
Dump abstraction

\[ \phi(\text{Trace}) \xrightarrow{\gamma_\text{Dump}} \text{Dump} \]

\[ \gamma_\text{Dump} \overset{\phi(\text{Dump})}{\longrightarrow} \text{Dump} \]

\[ \gamma_\text{State} = \{ d \in \text{Dump} \mid d = (p, a) :: _ \}, \]

\[ p = \eta_\text{Dump} d, a \in \gamma_\text{Addr}(p) \]

\[ \eta_\text{Dump} : \text{Dump} \rightarrow \text{ProcId} \]

\[ \eta_\text{Dump} ((p, a) :: _) = p \]

States of traces without order

\[ \phi(\text{Trace}) \xrightarrow{\gamma_\text{State}} \phi(\text{State}) \]

\[ \gamma_\text{State} \Sigma = \{(\sigma_1, \cdots, \sigma_n) \mid \forall i \in [1, \cdots, n] : \sigma_i \in \Sigma \} \]

3.1.4 Memory abstraction

Memories

\[ \phi(\text{Memory}) \xrightarrow{\gamma_\text{Memory}} \text{Memory} \]

\[ \gamma_\text{Memory} (\hat{M}, \hat{I}) = \{(M, I) \in \text{Memory} \mid M \in \gamma_\text{Map} \hat{M}, I \in \gamma_\text{Alloc} \hat{I} \} \]

Value mappings

\[ \phi(\text{Map}) \xrightarrow{\gamma_\text{Map}} \text{Map} \]

\[ \gamma_\text{Map} \hat{M} = \{ M \in \text{Map} \mid \forall a \mapsto v \in M, \hat{a} \mapsto \hat{V} \in \hat{M} : \]

\[ \eta_\text{Addr} a \subset \hat{a} \mapsto v \in \gamma_\text{Val} \hat{V}, \]

\[ \exists \hat{V} \in \text{dom}(\hat{M}) : \eta_\text{Addr} a \subset \hat{a}' \} \]

\[ \eta_\text{Addr} : \text{Addr} \rightarrow \hat{\text{Addr}} \]

\[ \eta_\text{Addr} X = X \]

\[ \eta_\text{Addr}(d, x) = (\eta_\text{Dump} d, x) \]

\[ \eta_\text{Addr}(r, n) = (\eta_\text{Region} r, [v, r]) \]

\[ \eta_\text{Addr}(r, f) = (\eta_\text{Region} r, f) \]

Allocation information

\[ \phi(\text{Alloc}) \xrightarrow{\gamma_\text{Alloc}} \text{Alloc} \]

\[ \gamma_\text{Alloc} \hat{I} = \{ I \in \text{Alloc} \mid \forall r \mapsto i \in I : \]

\[ i \in \gamma_\text{Z} \hat{h} \cup \hat{F}, (\hat{h}, \hat{F}) = I(\eta_\text{Region} r) \} \]

\[ \eta_\text{Region} : \text{Region} \rightarrow \hat{\text{Region}} \]

\[ \eta_\text{Region} (_, I) = t \]

3.1.5 Value abstraction

Values

\[ \phi(\text{Val}) \xrightarrow{\gamma_\text{Val}} \phi(\hat{\text{Val}}) = \hat{\text{Value}} \]

\[ \gamma_\text{Val} \hat{V} = \bigcup \{ \gamma_\text{Z} \hat{h} \mid \hat{h} \in \hat{V} \cap \hat{Z} \} \cup \gamma_\text{Addr} (\hat{V} - \hat{Z}) \]
3.2 Abstract semantics

Let us define the abstract semantics \( \hat{P} \) of a \( G \) program \( P \) from some \( S_0 \in \text{Summary} \) that covers all initial states, as the least fixed point of the abstract semantic transfer function \( \hat{\mathcal{T}} \) \( P \) greater than or equal to \( S_0 \).

\[
\hat{P} : \text{Summary} \rightarrow \text{Summary}
\]

\[
\hat{P} S_0 = \text{lfp}_{S_0}(\hat{\mathcal{T}} P)
\]

where

\[
\hat{\mathcal{T}} : \text{Pg m} \rightarrow \text{Summary} \rightarrow \text{Summary}
\]

\[
\hat{\mathcal{T}} P (\hat{\mathcal{G}}, \hat{T}, \hat{D}) = (\hat{\mathcal{G}}, \hat{T}, \hat{D})
\]

where

\[
\hat{\mathcal{G}}' = \hat{\mathcal{G}} \cup \text{Flows}_{\mathcal{P}}
\]

\[
\cup \{(b^p, \text{ENTRY}_{\mathcal{P}}), (\text{EXIT}_{\mathcal{P}}, b') \mid b^p = \text{CALL}(\alpha, e_0, \ldots),
\]

\[
(\alpha, b^p) \in \hat{\mathcal{G}}, (b^p, b') \in \text{Resumes}_{\mathcal{P}},
\]

\[
b^p \mapsto \hat{m} \in \hat{T}, p' \in \hat{A} p \hat{m} e_0, (p', x) \in P,
\]

\[
(\hat{M}, \hat{l}) = \hat{m}, \hat{m}' = (\hat{M}[\hat{A} p' \hat{m} x \mapsto \hat{V} p \hat{m} e_1], \hat{l})
\]

\[
\sqcup \{ \text{ENTRY}_{\mathcal{P}}, \text{EXIT}_{\mathcal{P}} \}
\]

\[
\sqcup \{ b' \mapsto \hat{m}' \mid b \in \text{ENTRY}_{\mathcal{P}} \}
\]

\[
\sqcup \{ b' \mapsto \hat{m}' \mid b^p = (c_1, \ldots, c_n),
\]

\[
(b^p, b') \in \hat{\mathcal{G}},
\]

\[
b^p \mapsto \hat{m} \in \hat{T}, \hat{m}' = (\hat{R} p c_1; \ldots; \hat{R} p c_n) \hat{m}
\]

\[
\hat{\mathcal{D}}' = \hat{\mathcal{D}} \cup \{ p' \mapsto \hat{A} p \hat{m} e \mid b^p = \text{CALL}(c, e_0, \ldots),
\]

\[
(\alpha, b^p) \in \hat{\mathcal{G}},
\]

\[
b^p \mapsto \hat{m} \in \hat{T}, p' \in \hat{A} p \hat{m} e_0, (p', x) \in P
\]

where \( \hat{R} \) \( D \) \( p \) \( c \) \( m \), \( V \) \( p \) \( m \) \( e \) and \( \hat{A} \) \( p \) \( m \) \( e \) respectively defines the abstract semantics of command \( c \), expression \( e \) under \( p \in \text{ProcId} \), \( D \) \( D \) \( p \) \( c \) \( m \), and \( \hat{m} \in \text{Memory} \).
3.2.1 Reaction

\( \hat{R} \) defines how a single command \( e \) changes the abstract memory \( \hat{m} \).

\[
\hat{R} : \text{Dump} \rightarrow \text{ProcId} \rightarrow \text{Cmd} \rightarrow \text{Memory} \rightarrow \text{Memory}
\]

\[
\hat{R} p \text{ SET}(e, v) (\hat{M}, \hat{I}) = (\hat{M}[\hat{A} p \hat{M} e_0 \mapsto \hat{V} p \hat{M} e], \hat{I})
\]

\[
\hat{R} p \text{ ESCAPE}(e) (\hat{M}, \hat{I}) = (\hat{M}[\hat{D}(p) \mapsto \hat{V} p \hat{M} e], \hat{I})
\]

\[
\hat{R} p \text{ ASSUME}(\gamma) (\hat{M}, \hat{I}) = \hat{V} p \gamma (\hat{M}, \hat{I})
\]

\[
\hat{R} p \text{ ALLOC}(e, [c]) (\hat{M}, \hat{I}) = (\hat{M}[\hat{A} p \hat{M} e_0 \mapsto ([\hat{t}], [0, 0]))
\]


\[
\hat{R} p \text{ FREE}(e) (\hat{M}, \hat{I}) = (\hat{M}, \hat{I})
\]

3.2.2 Lookup and update

Abstract value mapping \( \hat{M} \in \text{Map} \) is always used with set of abstract addresses \( \hat{A} \in \text{Address} \) in these semantics definitions. Hence, let us define two abbreviations here that extend lookup and update on \( \text{Map} = \text{Addr} \xrightarrow{\alpha} \text{Value} \) to \( \text{Address} \rightarrow \text{Value} \).

First, let us define set of overlapping addresses as following.

\[
\text{overlap}(\hat{a}, \hat{A}) = \{ \hat{a}' \in \hat{A} | \hat{a} \cap \hat{a}' \neq \emptyset \}
\]

Lookup with \( \hat{A} \) is defined as following.

\[
\hat{M} ? \hat{A} = \bigcup \{ (\hat{M}(\hat{a}') \mid \hat{a}' \in \text{overlap}(\hat{a}, \text{dom}(\hat{M})), \hat{a} \in \hat{A}) \}
\]

Update with \( \hat{A} \) is defined as following.

\[
\hat{M}[\hat{A} \mapsto \hat{V}] = \hat{M}
\]

\[
\begin{aligned}
\bigcup \{ & \hat{a}' \mapsto \hat{V} \mid \hat{a}' \in \text{overlap}(\hat{a}, \text{dom}(\hat{M})), \hat{a} \in \hat{A} \\
\bigcup \{ & \hat{a} \mapsto \hat{V} \mid \hat{a} \in \hat{A} - \text{Region} \times \text{Index} \\
\bigcup \{ & (\hat{t}, [-\infty, +\infty]) \mapsto \hat{V} \mid (\hat{r}, _) \in \hat{A} \\
\end{aligned}
\]

3.2.3 Value

\( \hat{V} \) defines abstract value for each expression of a procedure \( p \), which is an abstraction of possible values such expression can compute under some value mapping \( \hat{M} \).

\[
\hat{V} : \text{ProcId} \rightarrow \text{Map} \rightarrow \text{Expr} \rightarrow \text{Value}
\]

\[
\begin{aligned}
\hat{V} p \hat{M} n &= \{n, n\} \\
\hat{V} p \hat{M} x &= \{x\} \\
\hat{V} p \hat{M} e_1 \ast e_2 &= \hat{V} p \hat{M} e_1 \ast \hat{V} p \hat{M} e_2 \\
\hat{V} p \hat{M} e. f &= \{ (\hat{r}, f) \mid (\hat{r}, _) \in \hat{A} p \hat{M} e \} \\
\hat{V} p \hat{M} e \text{?} &= \hat{M} ? \hat{A} p \hat{M} e
\end{aligned}
\]

For convenience, let us overload \( \hat{V} \) and also use it on \( \text{Memory} \).

\[
\hat{\hat{V}} : \text{ProcId} \rightarrow \text{Memory} \rightarrow \text{Expr} \rightarrow \text{Value}
\]

\[
\begin{aligned}
\hat{\hat{V}} p (\hat{M}, _) e &= \hat{V} p \hat{M} e
\end{aligned}
\]
3.2.4 Address

\( \hat{A} \) defines abstract address for each expression.

\[
\hat{A} : \text{Procl} \rightarrow \text{Map} \rightarrow \text{Expr} \rightarrow \text{Address}
\]

\[
\hat{A} p \hat{M} e = \hat{V} p \hat{M} e
\]

3.2.5 Pruning

Pruning \( \hat{P} \) defines a refined abstract memory according to the relation being assumed.

\[
P : \text{Procl} \rightarrow \text{Rel} \rightarrow \text{Memory} \rightarrow \text{Memory}
\]

\[
P p r (\hat{M}, \hat{I}) = (\{(\hat{M}[\hat{A} \leftarrow \hat{V}] | \hat{A} = \hat{A} p (\hat{M}, \hat{I}) e, e \in L r, s e \circ e' \in N c r \cup N c', \hat{V} = \hat{M} ? \hat{A} \cap S e (\hat{V} p (\hat{M}, \hat{I}) e'), \hat{V} \neq \hat{1}_{\text{value}}) \cup (\hat{M} \{ e \circ e' = r, \hat{V} p (\hat{M}, \hat{I}) e \cap S e (\hat{V} p (\hat{M}, \hat{I}) e') \neq \hat{1}_{\text{value}}\}, \hat{I})
\]

\( S_e \) defines an abstract value which is an abstraction of possible concrete values that can satisfy the relation \( \circ \) with a value among the meaning of the given abstract value.

\[
S_e : \text{Value} \rightarrow \text{Value}
\]

\[
S_{\equiv} \hat{V} = \hat{V}
\]

\[
S_1 = \hat{V} = \hat{1}_{\text{value}}
\]

\[
S_{<} \hat{V} = \{ [[-\infty, u - 1]] | ([r, u]) \in \hat{V} \}
\]

\[
\cup \{ (\hat{r}, [-\infty, u - 1]) | (\hat{r}, [l, u]) \in \hat{V} \}
\]

\[
S_{<} \hat{V} = \{ [[l, +\infty]] | ([l, u]) \in \hat{V} \}
\]

\[
\cup \{ (\hat{r}, [l, +\infty]) | (\hat{r}, [l, u]) \in \hat{V} \}
\]

\[
S_{>} \hat{V} = \{ [[0, +\infty]] | ([l, u]) \in \hat{V} \}
\]

\[
\cup \{ (\hat{r}, [0, +\infty]) | (\hat{r}, [l, u]) \in \hat{V} \}
\]

\[
S_{<} \hat{V} = \{ [[-\infty, u]] | ([l, u]) \in \hat{V} \}
\]

\[
\cup \{ (\hat{r}, [-\infty, u]) | (\hat{r}, [l, u]) \in \hat{V} \}
\]

\( L \) gives a set of expressions that computes an address whose value mapping in the memory can be modified.

\[
L : \text{Expr} \rightarrow \varphi(\text{Expr})
\]

\[
L s e = [e]
\]

\[
L e \circ e' = L e \cup L e'
\]

\[
L_\_ = \emptyset
\]

\[
\varphi L : \text{Rel} \rightarrow \varphi(\text{Rel})
\]

\[
L e \circ e' = L e \cup L e'
\]

\( N e \) defines all equivalent relations with the given one whose left hand side is \( s e \).

\[
N : \text{Expr} \rightarrow \text{Rel} \rightarrow \varphi(\text{Rel})
\]

\[
N s e \circ e' = \{ s e \circ e' \}
\]

\[
N e (e_1 + e_2) \circ e_3 = N e (e_1 \circ (e_2 - e_1) \cup N e (e_2 \circ (e_3 - e_1))
\]

\[
N e (e_1 - e_2) \circ e_3 = N e (e_1 \circ (e_2 + e_3) \cup N e (e_2 \circ (e_1 - e_3))
\]

\[
N e (n^* e_1) \circ e_2 = N e (e_1 * n) \circ e_2
\]

\[
N e (e_1 * n) \circ e_2 = N e (e_1 \circ (e_2 / n)) \cap n > 0
\]

\[
N e (e_1 / n) \circ e_2 = N e (e_1 \circ (e_1 * n)) \cap n > 0
\]

\[
N e (e_1 / n) \circ e_2 = N e (e_1 \circ (e_2 / n)) \cap n < 0
\]

\[
N e (e_1 / n) \circ e_2 = N e (e_1 \circ (e_2 * n)) \cap n < 0
\]

\[
N e_\_ = \emptyset
\]
\( r' \) is the relation that has the same meaning as \( r \) but its left and right hand sides swapped.

\[
(\preceq \circ \preceq')' = \preceq' \circ \preceq \quad \text{where}
\begin{align*}
\preceq' &= =, \quad >, \\
\preceq' &= =, \quad >.
\end{align*}
\]

3.2.6 Operations

\[
\begin{align*}
\{\hat{\nu}, \hat{\eta}, \hat{\gamma}\} & : \text{Value} \times \text{Value} \to \text{Value} \\
\hat{\nu} \circ \hat{\eta} &= \{\hat{\nu} \circ \hat{\eta} | \hat{\nu} \in \hat{\nu}, \hat{\eta} \in \hat{\eta}\} \\
\hat{\nu} \circ \hat{\eta} &= \{\hat{\nu} \circ \hat{\eta} | \hat{\nu} \in \hat{\nu}, \hat{\eta} \in \hat{\eta}\}
\end{align*}
\]

\[
\begin{align*}
[a, b] \circ[c, d] &= \{a + c, b + d\} \quad \text{where} \forall z \in Z: \\
&= -\infty + z = z + -\infty = -\infty + -\infty = -\infty, \\
&= +\infty + z = z + +\infty = +\infty + +\infty = +\infty \\
[a, b] \circ[c, d] &= \{a - d, b - c\} \quad \text{where} \forall z \in Z: \\
&= -\infty - z = z - -\infty = -\infty - +\infty = -\infty, \\
&= +\infty - z = z - +\infty = +\infty - -\infty = +\infty \\
[a, b] \circ[c, d] &= \{\min X, \max X\} \quad \text{where} \\
X &= [a \times c, a \times d, b \times c, b \times d] \\
[a, b] \circ[c, d] &= [-\infty, +\infty] \quad \text{if} \ c \leq 0 \leq d \\
&= [\min X, \max X] \quad \text{otherwise where} \\
X &= [a \div c, a \div d, b \div c, b \div d]
\end{align*}
\]

4 Soundness

4.1 Semantics

Theorem 1 Abstract semantics is sound, i.e.

\[
\Sigma \subseteq \gamma \hat{\delta} \\
\implies \forall P \in Pgm : \text{lfp}_2(\hat{\delta} P) \subseteq \gamma (\text{lfp}_3(\hat{\delta} P))
\]

Proof Since \( \hat{\delta} P \) is monotone, by fixpoint transfer theorem[8, theorem 7.1.0.4] with Lemma 1, we have the soundness of abstract semantics. \( \square \)

Lemma 1 Transition \( \hat{\delta} \) is sound, i.e.

\[
T \subseteq \gamma \hat{\delta} \\
\implies \forall P \in Pgm : P T \subseteq \gamma (\hat{\delta} P \hat{\delta})
\]

Proof Since \( \hat{\delta} P \) and \( \hat{\delta} P \) are both extensive, we only need to check all new trace \( \tau' \in T \) such that \( \tau' \notin T \) is also in \( \gamma (\hat{\delta} P \hat{\delta}) \). If \( \tau = (\sigma_1, \cdots, \sigma) \in T \), then by definition of \( \hat{\delta} \), new traces are

\[
\tau' = (\sigma_1, \cdots, \sigma, \sigma') \in T.
\]
such that $\sigma \rightarrow \sigma'$. So, let us consider each case of transition,

$$(b, m, d) \rightarrow (b', m', d')$$

where $\sigma = (b, m, d)$ and $\sigma' = (b', m', d')$. Let us use $(G, T, D)$ to refer $S$ and $(G', T', D') = S'$ to refer $\hat{F} P \hat{S}$. We are going to check whether $\tau' \in \gamma (G', T', D')$ holds when $\tau \in \gamma (G, T, D)$.

By Lemma 5, Hence, we have $\tau_m \rightarrow \tau$.

By Corollary 1, $\tau_m \rightarrow \tau = \hat{T}$.

By definition of $\hat{D}$, we have

$$(b, \text{ENTRY}_{p'}) = (b, b') \in \hat{G}'.$$

Now, by definition of $\rightarrow$, $d' = (p', b, A \rightarrow m e) : d$.

By definition, $\eta_{\text{Dump}} d' = p'$. Because of the following,

$$A \rightarrow m e \in \gamma_{\text{Addr}} (A p \rightarrow m e)$$

(Corollary 1)

$$\subseteq \gamma_{\text{Addr}} (D'(p'))$$

(definition of $\hat{F}$, $\gamma_{\text{Addr}}$ is monotone)

definition of $\gamma_{\text{Dump}}$ gives,

$$d' \in \gamma_{\text{Dump}} D'.$$

By definition of $\rightarrow$,

$$m' = (M[d', x] \mapsto \forall v \rightarrow m e_1], c, I)$$

where $(p', x) \in P$. By definition of $A$ and $\gamma_{\text{Addr}}$,

$$(d', x) = A d' \rightarrow m x$$

(definition of $A$)

$$\in \gamma_{\text{Addr}} (A p' \rightarrow m x)$$

(Corollary 1)

By Lemma 6,

$$\forall v \rightarrow m e_1 \in \gamma_{\text{Val}} (\forall v p \rightarrow m e_1).$$

By Lemma 5,

$$M[d', x] \mapsto \forall v \rightarrow m e_1] \in \gamma_{\text{Map}} M[A p' \rightarrow m x] \mapsto \forall v p \rightarrow m e_1],$$

and therefore,

$$m' \in \gamma_{\text{Memory}} m' = \gamma_{\text{Memory}} (M[A p' \rightarrow m x] \mapsto \forall v p \rightarrow m e_1], I).$$

Since $\gamma_{\text{Memory}}$ is monotone and by definition of $\hat{F}$,

$$m' \in \gamma_{\text{Memory}} m' \in \gamma_{\text{Memory}} \hat{T}'(\text{ENTRY}_P) = \gamma_{\text{Memory}} \hat{T}'(b').$$

Hence, we have $\tau' \in \gamma (G', T', D')$. 
When $b = \text{ENTRY}_p$, by definition of $\rightarrow$, $(\text{ENTRY}_p, b') \in \text{Flows}_p$, $m' = m$ and $d' = d$. Since $\text{Flows}_p \subseteq \hat{\mathcal{C}}'$, $(\text{ENTRY}_p, b') \in \hat{\mathcal{C}}'$. Since $\gamma_{\text{Memory}}$ is monotone,

$$m' = m \in \gamma_{\text{Memory}} \hat{T}(b') \subseteq \gamma_{\text{Memory}} \check{T}(b').$$

Since $\gamma_{\text{Dump}}$ is monotone,

$$d' = d \in \gamma_{\text{Dump}} \hat{D} \subseteq \gamma_{\text{Dump}} \check{D}'.$$

Therefore, $\tau' \in \gamma (\hat{\mathcal{C}}', \hat{T}', \hat{D}')$.

When $b = \text{EXIT}_p$, by definition of $\rightarrow$,

$$\langle p', b', \_ \rangle :: d' = d \quad \text{and} \quad m' = m$$

The only case where $(p', b', \_)$ is pushed onto $d'$ is $b'' = \text{CALL}(e, c_0, c_1)$. Therefore, $(b'', b') \in \text{Resumes}_p$ and by definition of $\hat{F}$, $(\text{ENTRY}_p, b') \in \hat{\mathcal{C}}$. Since $d'$ already appeared in the $\tau$ with $b''$, by monotony of $\gamma_{\text{Dump}}$, we have

$$d' \in \gamma_{\text{Dump}} \hat{D} \subseteq \gamma_{\text{Dump}} \check{D}'.$$

By monotony of $\gamma_{\text{Memory}}$, we have

$$m' = m \in \gamma_{\text{Memory}} \hat{T}(b') \subseteq \gamma_{\text{Memory}} \check{T}(b').$$

Therefore, $\tau' \in \gamma (\hat{\mathcal{C}}', \hat{T}', \hat{D}')$.

When $b = (c_1, \cdots, c_n)$, let us first define each $m_i$ and $\hat{m}_i$ as following.

$$m_1 = m \quad \hat{m}_1 = \hat{m}$$
$$m_{i+1} = R \hat{d} c_i m_i \quad \hat{m}_{i+1} = \hat{R} \hat{D} p c_i \hat{m}_i$$
$$m' = m_{n+1} \quad \hat{m}' = \hat{m}_{n+1}$$

where $\hat{m} = \hat{T}(b)$. We are going to show $m \in \gamma_{\text{Memory}} \hat{m}'$ by induction on the length of commands.

Since $\tau \in \gamma \hat{S}$ and $\hat{m} = \hat{T}(b)$, by definition of $\gamma$, $m \in \gamma_{\text{Memory}} \hat{m}$, we have the base case,

$$m_1 \in \gamma_{\text{Memory}} \hat{m}_1.$$

By Lemma 2, we have the inductive case,

$$m_i \in \gamma_{\text{Memory}} \hat{m}_i \implies R \hat{d} c_i m_i \in \gamma_{\text{Memory}} (\hat{R} \hat{D} p c_i \hat{m}_i) \implies m_{i+1} \in \gamma_{\text{Memory}} \hat{m}_{i+1}$$

Hence, by induction, we have $m' \in \gamma_{\text{Memory}} \hat{m}'$.

Definition of $\rightarrow$ gives $d' = d$ and $(b, b') \in \text{Flows}_p$. Since $\text{Flows}_p \subseteq \hat{\mathcal{C}}'$, $(b, b') \in \hat{\mathcal{C}}'$. Since $\gamma_{\text{Dump}}$ is monotone, $d' = d \in \gamma_{\text{Dump}} \hat{D} \subseteq \gamma_{\text{Dump}} \check{D}'$.

$$m' \in \gamma_{\text{Memory}} \hat{m}' \subseteq \gamma_{\text{Memory}} (\hat{T}(b') \sqcup \hat{m}') \subseteq \gamma_{\text{Memory}} \check{T}(b')$$

Therefore $\tau' \in \gamma (\hat{\mathcal{C}}', \hat{T}', \hat{D}')$.

From the above analysis on every $\tau'$ that can be generated from possible transitions, we have

$$\tau' \in \gamma (\hat{\mathcal{C}}', \hat{T}', \hat{D}').$$

Thus,

$$\mathcal{F} \mathcal{P} \mathcal{T} \subseteq \gamma (\hat{\mathcal{F}} \mathcal{P} \hat{\mathcal{S}}).$$
4.2 Reaction

Lemma 2 Reaction $\hat{R}$ is sound, i.e.

\[ d \in \gamma_{\text{Dump}} \hat{D}, \]
\[ \eta_{\text{Dump}} d = p, \]
\[ m \in \gamma_{\text{Memory}} \hat{m} \]
\[ \implies \forall c \in \text{Cmd} : R d c m \in \gamma_{\text{Memory}} (\hat{R} \hat{D} p c \hat{m}) \]

Proof Let us consider each case of $c$. Let $(M, c, I) = m$ and $(\hat{M}, \hat{I}) = \hat{m}$ where $M \in \gamma_{\text{Map}} \hat{M}$ and $I \in \gamma_{\text{Alloc}} \hat{I}$.

When $c = \text{SET}(e_a, e)$, by definition of $R$,

\[ \hat{R} d \text{SET}(e_a, e) (M, c, I) = (M[\mathcal{A} d M e_a \mapsto \mathcal{V} d M e], c, I). \]

By Corollary 1,

\[ \mathcal{A} d M e_a \in \gamma_{\text{Addr}} (\hat{\mathcal{A}} p \hat{M} e_a) \]

and by Lemma 6,

\[ \mathcal{V} d M e \in \gamma_{\text{Val}} (\hat{\mathcal{V}} p \hat{M} e). \]

By Lemma 5,

\[ M[\mathcal{A} d M e_a \mapsto \mathcal{V} d M e] \in \gamma_{\text{Map}} \hat{M}[\hat{A} p \hat{M} e_a \mapsto \hat{V} p \hat{M} e] \]

and hence,

\[ (M[\mathcal{A} d M e_a \mapsto \mathcal{V} d M e], c, I) \in \gamma_{\text{Memory}} (\hat{M}[\hat{A} p \hat{M} e_a \mapsto \hat{V} p \hat{M} e], I) . \]

Therefore, by definition of $\hat{R}$,

\[ \hat{R} d \text{SET}(e_a, e) (M, c, I) \in \gamma_{\text{Memory}} (\hat{R} \hat{D} p \text{SET}(e_a, e) (\hat{M}, \hat{I})). \]

When $c = \text{ESCAPE}(e)$, by definition of $R$,

\[ \hat{R} d \text{ESCAPE}(e) (M, c, I) = (M[a \mapsto \mathcal{V} d M e], c, I) \]

where $(\_ \_ \_ a) :: \_ = d$. By Lemma 6,

\[ \mathcal{V} d M e \in \gamma_{\text{Val}} (\hat{\mathcal{V}} p \hat{M} e). \]

Since $d \in \gamma_{\text{Dump}} \hat{D}$ and $\eta_{\text{Dump}} d = p$, by definition of $\gamma_{\text{Dump}}$,

\[ a \in \gamma_{\text{Addr}} \hat{D}(p). \]

By Lemma 5,

\[ M[a \mapsto \mathcal{V} d M e] \in \gamma_{\text{Map}} \hat{M}[\hat{D}(p) \mapsto \hat{V} p \hat{M} e] \]

and hence,

\[ (M[a \mapsto \mathcal{V} d M e], c, I) \in \gamma_{\text{Memory}} (\hat{M}[\hat{D}(p) \mapsto \hat{V} p \hat{M} e], I) . \]

Therefore, by definition of $\hat{R}$,

\[ \hat{R} d \text{ESCAPE}(e) (M, c, I) \in \gamma_{\text{Memory}} (\hat{R} \hat{D} p \text{ESCAPE}(e) (\hat{M}, \hat{I})). \]

When $c = \text{ASSUME}(r)$,

\[ \hat{R} d \text{ASSUME}(r) (M, c, I) = (M, c, I) \]

where $M, l \models r$. By Lemma 3,

\[ (M, c, I) \in \gamma_{\text{Memory}} (\mathcal{P} p r (\hat{M}, \hat{I})). \]
Therefore, by definition of $\hat{R}$,
\[
\mathcal{R} d \text{	exttt{ASSUME}}(r) \ (M, c, l) \in \gamma_{\text{Memory}} (\hat{R} \hat{D} p \text{\texttt{ASSUME}}(r) (\hat{M}, \hat{l})).
\]

When $c = \text{\texttt{ALLOC}}(a, [c])$, by definition of $\mathcal{R}$,
\[
\mathcal{R} d \text{\texttt{ALLOC}}(c, [c])_t (M, c, l) = (M[A d M e_d \mapsto (r, 0)], c + 1, I[r \mapsto \mathcal{V} d M c])
\]
where $r = (c, t)$. By definition of $\eta_{\text{Region}}$,
\[
\eta_{\text{Region}} \ r = t.
\]

By Corollary 1,
\[
\mathcal{A} d M e_d \in \gamma_{\text{Addr}} (\hat{A} p \hat{M} e_d)
\]
and by definition of $\gamma_{\text{Val}}$,
\[(r, 0) \in \gamma_{\text{Val}} [(\hat{r}, [0, 0])].\]

By Lemma 5,
\[
M[A d M e_d \mapsto (r, 0)] \in \gamma_{\text{Map}} \hat{M}[A p \hat{M} e_d \mapsto [(\hat{r}, [0, 0])].
\]

By Lemma 6,
\[
\mathcal{V} d M e \in \gamma_{\text{Val}} (\hat{\mathcal{V}} p \hat{M} e)
\]
and hence,
\[
I[r \mapsto \mathcal{V} d M e] \in \gamma_{\text{Alloc}} \hat{I}[\hat{r} \mapsto \hat{I}(\hat{r}) \sqcup (\hat{\mathcal{V}} p \hat{M} e, 0)].
\]

Therefore, we have
\[
(M[A d M e_d \mapsto (r, 0)], c + 1, I[r \mapsto \mathcal{V} d M e]) \in \\
\gamma_{\text{Memory}} (\hat{M}[A p \hat{M} e_d \mapsto [(\hat{r}, [0, 0])], \hat{I}[\hat{r} \mapsto \hat{I}(\hat{r}) \sqcup (\hat{\mathcal{V}} p \hat{M} e, 0)]).
\]

By definition of $\hat{R}$,
\[
\mathcal{R} d \text{\texttt{ALLOC}}(c, [c])_t (M, c, l) \in \gamma_{\text{Memory}} (\hat{R} \hat{D} p \text{\texttt{ALLOC}}(c, [c])_t (\hat{M}, \hat{l})).
\]

When $c = \text{\texttt{ALLOC}}(a, \{\hat{f}\})$, by definition of $\mathcal{R}$,
\[
\mathcal{R} d \text{\texttt{ALLOC}}(c, \{\hat{f}\})_t (M, c, l) = (M[A d M e_d \mapsto (r, f_1)], c + 1, I[r \mapsto \hat{f}])
\]
where $r = (c, t)$. By definition of $\eta_{\text{Region}}$,
\[
\eta_{\text{Region}} \ r = t.
\]

By Corollary 1,
\[
\mathcal{A} d M e_d \in \gamma_{\text{Addr}} (\hat{A} p \hat{M} e_d)
\]
and by definition of $\gamma_{\text{Val}}$,
\[(r, f_1) \in \gamma_{\text{Val}} [(\hat{r}, f_1)].\]

By Lemma 5,
\[
M[A d M e_d \mapsto (r, f_1)] \in \gamma_{\text{Map}} \hat{M}[A p \hat{M} e_d \mapsto [(\hat{r}, f_1)].
\]

By definition of $\gamma_{\text{Alloc}}$,
\[
I[r \mapsto \hat{f}] \in \gamma_{\text{Alloc}} \hat{I}[\hat{r} \mapsto \hat{I}(\hat{r}) \sqcup (\hat{\mathcal{A}} p \hat{M} e_d, 0)]
\]
Therefore, we have
\[
(M[A d M e_d \mapsto (r, f_1)], c + 1, I[r \mapsto \hat{f}]) \in \\
\gamma_{\text{Memory}} (\hat{M}[A p \hat{M} e_d \mapsto [(\hat{r}, f_1)], \hat{I}[\hat{r} \mapsto \hat{I}(\hat{r}) \sqcup (\hat{\mathcal{A}} p \hat{M} e_d, 0)]).
By definition of \( R \),
\[
R \downarrow \text{ALLOC}(e_a, \{\vec{f}\})_1 (M, c, I) \in \gamma_{\text{Memory}} (\hat{R} \downarrow \text{p ALLOC}(e_a, \{\vec{f}\})_1 (\hat{M}, \hat{I})).
\]

When \( c = \text{FREE}(e) \), by definition of \( R \),
\[
R \downarrow \text{FREE}(e) (M, c, I) = (M - [a], c, I - [a])
\]
where \( a = R \downarrow M e \). By definition of \( \gamma_{\text{Map}} \) and \( M \in \gamma_{\text{Map}} \hat{M} \),
\[
I - [a] \in \gamma_{\text{Alloc} \hat{I}}.
\]
Similarly by definition of \( \gamma_{\text{Alloc}} \) and \( I \in \gamma_{\text{Alloc} \hat{I}} \),
\[
I - [a] \in \gamma_{\text{Alloc} \hat{I}}.
\]
Therefore, we have
\[
(M - [a], c, I - [a]) \in \gamma_{\text{Memory}} (\hat{M}, \hat{I}).
\]
By definition of \( \hat{R} \),
\[
R \downarrow \text{FREE}(e) (M, c, I) \in \gamma_{\text{Memory}} (\hat{R} \downarrow \text{p FREE}(e) (\hat{M}, \hat{I})).
\]

Thus, for any case of \( c \in \text{Cmd} \), we have
\[
R \downarrow c (M, c, I) \in \gamma_{\text{Memory}} (\hat{R} \downarrow \text{p c} (\hat{M}, \hat{I})).
\]

\[\square\]

**Lemma 3** Pruning \( \hat{P} \) is sound, i.e.
\[
d, m \models r,
\quad p = \eta_{\text{Dump}} d,
\quad m \in \gamma_{\text{Memory} \hat{m}}
\implies m \in \gamma_{\text{Memory}} (\hat{P} \downarrow \text{p r} \hat{m})
\]

**Proof** First, we show a proposition of \( S \).
\[
d, m \models c \circ c'
\implies \forall \text{p m e} \cap S, (\forall \text{p m e'}) \neq \perp_{\text{Value}}
\]
By definition of \( \models \), the premise becomes
\[
\forall d m e \circ \forall d m e'.
\]
By Lemma 6,
\[
\forall d m e \in \gamma_{\text{Val}} (\forall \text{p m e}),
\quad \forall d m e' \in \gamma_{\text{Val}} (\forall \text{p m e'}).
\]
Since \( \gamma_{\text{Val}} \) is multiplicative, we may show the following instead.
\[
\forall_{\text{Val}} (\forall \text{p m e}) \cap \forall_{\text{Val}} (S, (\forall \text{p m e'})) \neq \emptyset.
\]
So, we will actually show the following for each case of \( \circ \).
\[
\forall d m e \in \gamma_{\text{Val}} (S, (\forall \text{p m e'})).
When $\diamond = =$, 
\[
\forall \ p \ m \ e \quad \forall \ p \ m' \ e' \\
(\text{definition of } \models) \\
\in \gamma_\text{Val} (\hat{V} p \hat{\mathfrak{m}} e') \\
(\text{assumption}) \\
= \gamma_\text{Val} (\hat{S} = (\hat{V} p \hat{\mathfrak{m}} e')) \\
(\text{definition of } S)
\]

When $\diamond = \neq$, 
\[
\forall \ p \ m \ e \\
\in \gamma_\text{Val} \hat{\top} \text{Value} \\
(\text{assumption}) \\
= \gamma_\text{Val} (\hat{S} \neq (\hat{V} p \hat{\mathfrak{m}} e')) \\
(\text{definition of } S)
\]

When $\diamond <$, by definition of $\models$, 
\[
\forall \ p \ m \ e < \forall \ p \ m' \ e'.
\]

If $n = \forall \ p \ m' \ e'$, $l \leq n \leq u$, then 
\[
\forall \ p \ m \ e < n \leq u, \\
\forall \ p \ m \ e \leq u - 1,
\]
and by definition of $S$, 
\[
\forall \ p \ m \ e \in \gamma_\text{Val} (\hat{S} < (\hat{V} p \hat{\mathfrak{m}} e')).
\]

Same argument applies when $(r, n) = \forall \ p \ m' \ e'$.

Other cases for $\diamond \in \{\leq, >, \geq\}$ are also justified by very similar arguments.

Next, we check relations defined by $N$, and transpositioned relation are all equivalent with the original one.
\[
d, m | r' \iff d, m | r \iff \forall r' \in N \ e : d, m | r'
\]

The transposition $r'$ is obviously the equivalent relation to $r$, since it swaps the position of the two expressions, and changes the $\diamond$ to the corresponding inverted one.
\[
(e \circ e')' = e' \circ' e \\
\text{where} \\
= \Rightarrow = \Rightarrow ! = \Rightarrow ! = \\
< \Rightarrow > \Rightarrow > = \Rightarrow < \\
> \Rightarrow < \Rightarrow < = \Rightarrow
\]

Equivalence of relations defined by $N \ e$ is shown by structural induction. The first base case is obvious, since it is the identical relation.
\[
N \ e \ $ \ e \circ' e' = \{e \circ e'\}
\]

The second base case holds vacuously.
\[
N \ e \_ = \emptyset
\]

All other inductive cases hold because of the commutativity of arithmetic operators and equivalence of transposition of terms.

Now, we show the conclusion on $\hat{P}$ using previous arguments.

When there is no $e \in L$ such that $\hat{V} \neq 1_\text{Value}$, every consistent memory $m$ with relation $r$ is in $P p r \hat{\mathfrak{m}}$ by our first argument on $S$.

Otherwise, let us first recall the definition of $\sqcup$ that $\sqcup$ is the greatest one among the lower bounds.
\[
\forall a \in A : x \sqsubseteq a \implies x \sqsubseteq \sqcup A.
\]

By arguments on $N$ and $S$, and Lemma 5, greatestness of $\sqcup$, we have the same conclusion. $\square$
4.3 Memory manipulation

Lemma 4 Lookup is sound, i.e.

\[ a \in \gamma_{\text{Addr}} \\hat{A}, \]
\[ M \in \gamma_{\text{Map}} \\hat{M} \]
\[ \implies M(a) \in \gamma_{\text{Val}} (\hat{M} ? \hat{A}) \]

Proof First, suppose \( \gamma_{\text{Addr}} \\hat{A} \subseteq \gamma_{\text{Addr}} \text{dom}(\hat{M}) \). There exists \( a \in \gamma_{\text{Addr}} \\hat{A} \) but \( a \notin \gamma_{\text{Addr}} \text{dom}(\hat{M}) \). However, from \( M \in \gamma_{\text{Map}} \hat{M} \) and definition of \( \gamma_{\text{Map}} \), we have \( \text{dom}(M) \subseteq \gamma_{\text{Addr}} \text{dom}(\hat{M}) \). This means \( a \notin \gamma_{\text{Addr}} \text{dom}(M) \) cannot have mapping in \( M \). Hence \( M(a) \) is not defined, and the conclusion holds vacuously. So, we only need to consider the other possibility, \( a \in \gamma_{\text{Addr}} \text{dom}(\hat{M}) \).

From here on, without loss of generality, let us assume \( \gamma_{\text{Addr}} \\hat{A} \subseteq \gamma_{\text{Addr}} \text{dom}(\hat{M}) \). By definition of \( \gamma_{\text{Addr}} \),

\[ \forall a \in \gamma_{\text{Addr}} \\hat{A} : \eta_{\text{Addr}} a \subseteq \hat{a} \in \hat{A}, \eta_{\text{Addr}} a \subseteq \hat{a}' \in \text{dom}(\hat{M}). \]

Since \( \eta_{\text{Addr}} a \neq \bot_{\text{Addr}} \), and by greatestness of \( \subset \), i.e. \( a \subseteq b, a \subseteq c \implies a \subseteq b \cap c \), here we have,

\[ \bot_{\text{Addr}} \subseteq \eta_{\text{Addr}} a \subseteq \hat{a} \cap \hat{a}'. \]

Hence, \( \hat{a} \cap \hat{a}' \neq \bot_{\text{Addr}} \).

\[
M(a) \in \{ v | a \mapsto v \in M, a \in \gamma_{\text{Addr}} \\hat{A} \\
( a \in \gamma_{\text{Addr}} \\hat{A}) \}
\]
\[
= \{ v | a \mapsto v \in M, \eta_{\text{Addr}} a \subseteq \hat{a}, \hat{a} \in \hat{A} ; (\text{definition of } \gamma_{\text{Addr}})\}
\]
\[
\subseteq \{ v | a \mapsto v \in M, \eta_{\text{Addr}} a \subseteq \hat{a}, \hat{a} \in \hat{A}, \eta_{\text{Addr}} a \subseteq \hat{a}', \hat{a}' \mapsto \hat{v}' \in \hat{M}, v \in \gamma_{\text{Val}} \hat{v}' ; (M \in \gamma_{\text{Map}} \hat{M}, \gamma_{\text{Addr}} \\hat{A} \subseteq \gamma_{\text{Addr}} \text{dom}(\hat{M}))\}
\]
\[
\subseteq \gamma_{\text{Val}} \bigcup \{ \hat{v}' | a \mapsto v \in M, \eta_{\text{Addr}} a \subseteq \hat{a}, \hat{a} \in \hat{A}, \eta_{\text{Addr}} a \subseteq \hat{a}', \hat{a}' \mapsto \hat{v}' \in \hat{M}, v \in \gamma_{\text{Val}} \hat{v}' ; (\gamma_{\text{Val}} \text{ is monotone})\}
\]
\[
\subseteq \gamma_{\text{Val}} \bigcup \{ \hat{v}' | \hat{a}' \mapsto \hat{v}' \in \hat{M}, \hat{a} \in \hat{A}, \hat{a} \cap \hat{a}' \neq \bot_{\text{ Addr}} \}
\]

(Previous argument)
\[
= \gamma_{\text{Val}} \bigcup \{ \gamma_{\text{Map}} \hat{a}' | \hat{a}' \in \text{overlap}(\hat{a}, \text{dom}(\hat{M})), \hat{a} \in \hat{A} ; (\text{definition of overlap}(\hat{a}, \text{dom}(\hat{M})))\}
\]
\[
\subseteq \gamma_{\text{Val}} (\hat{M} ? \hat{A})
\]

(Definition of lookup, \( \gamma_{\text{Val}} \) is monotone)

Thus, lookup with set of addresses on abstract value mapping is sound. \( \square \)

Lemma 5 Update is sound, i.e.

\[ a \in \gamma_{\text{Addr}} \\hat{A}, \]
\[ v \in \gamma_{\text{Val}} \hat{V}, \]
\[ M \in \gamma_{\text{Map}} \hat{M} \]
\[ \implies M[a \mapsto v] \in \gamma_{\text{Map}} \hat{M}(\hat{A} \mapsto \hat{V}) \]

Proof By definition of update,

\[
\hat{M}(\hat{A} \mapsto \hat{V}) = \hat{M}
\]
\[
\cup \{ \hat{a}' \mapsto \hat{v} | \hat{a}' \in \text{overlap}(\hat{a}, \text{dom}(\hat{M})), \hat{a} \in \hat{A} \}
\]
\[
\cup \{ \hat{a} \mapsto \hat{v} | \hat{a} \in \hat{A} \setminus \text{Region} \times \text{Index} \}
\]
\[
\cup \{ (\hat{r}, [-\infty, +\infty]) \mapsto \hat{V} | (\hat{r}, _) \in \hat{A} \}
\]
In order to show that $M[a \mapsto v]$ is contained in $\gamma_{\text{Mapp}} M[\hat{A} \mapsto \hat{V}]$, we check the following.

$$\forall a' \mapsto v' \in M[a \mapsto v], \hat{a}' \mapsto \hat{V}' \in \hat{M}[\hat{A} \mapsto \hat{V}]:$$

$$\eta_{\text{Addr}} \hat{a}' \subseteq \hat{a}': \Rightarrow v' \in \gamma_{\text{Val}} \hat{V}'$$

$$\exists \hat{V}' \in \text{dom}(M[\hat{A} \mapsto \hat{V}]): \eta_{\text{Addr}} a' \subseteq \hat{a}'$$

Let $a' \mapsto v' \in M[a \mapsto v]$ and $\hat{a}' \mapsto \hat{V}' \in \hat{M}[\hat{A} \mapsto \hat{V}]$.

We will first check whether $\eta_{\text{Addr}} a' \subseteq \hat{a}'$ implies $v' \in \gamma_{\text{Val}} \hat{V}'$. So, let us assume $\eta_{\text{Addr}} a' \subseteq \hat{a}'$.

- If $a' \neq a$, then $a' \mapsto v' \in M$. Because $M \in \gamma_{\text{Mapp}} \hat{M} \subseteq \gamma_{\text{Mapp}} \hat{M}[\hat{A} \mapsto \hat{V}]$, we have $v' \in \gamma_{\text{Val}} \hat{V}'$.

- Otherwise, $a' = a$ and $v' = v$.

  - Suppose $\hat{a}' \in \hat{A}$. When $a \in \text{Region} \times \text{Index}$, $\hat{a}' = (\hat{\tau}, \_)$ for some $\hat{\tau} \in \text{Region}$. By $(\hat{\tau}, [-\infty, +\infty]) \mapsto \hat{V}$, we have $v' = v \in \gamma_{\text{Val}} \hat{V} \subseteq \gamma_{\text{Val}} \hat{V}'$.

  - Otherwise, by $\hat{a}' \mapsto \hat{V}$, $\hat{V} \subseteq \hat{V}'$. Since $\gamma_{\text{Val}}$ is monotone, $v' = v \in \gamma_{\text{Val}} \hat{V} \subseteq \gamma_{\text{Val}} \hat{V}'$.

Now, let us check whether there exists $\hat{a}' \in \text{dom}(\hat{M}[\hat{A} \mapsto \hat{V}])$ such that $\eta_{\text{Addr}} a' \subseteq \hat{a}'$.

- If $a' \neq a$, then $a' \mapsto v' \in M$. Because $M \in \gamma_{\text{Mapp}} \hat{M} \subseteq \gamma_{\text{Mapp}} \hat{M}[\hat{A} \mapsto \hat{V}]$, there exists $\hat{a}' \in \text{dom}(\hat{M}[\hat{A} \mapsto \hat{V}])$ such that $\eta_{\text{Addr}} a' \subseteq \hat{a}'$.

- Otherwise, $a' = a$. Since $a \in \gamma_{\text{Addr}} \hat{A}$, for some $\hat{a} \in \hat{A}$, $\eta_{\text{Addr}} a \subseteq \hat{a}$. From $\eta_{\text{Addr}} a \subseteq \hat{a}$ and $\eta_{\text{Addr}} \hat{a} \neq \bot_{\text{Addr}}$, we have $\hat{a} \cap \hat{a}' \neq \bot_{\text{Addr}}$. Therefore, $\hat{a}' \in \text{overlap}(\hat{a}, \text{dom}(\hat{M}))$. By $\hat{a}' \mapsto \hat{V}$, $\hat{V} \subseteq \hat{V}'$. Since $\gamma_{\text{Val}}$ is monotone, we have $v' = v \in \gamma_{\text{Val}} \hat{V} \subseteq \gamma_{\text{Val}} \hat{V}'$.

Thus, we have the conclusion,

$$M[a \mapsto v] \in \gamma_{\text{Mapp}} M[\hat{A} \mapsto \hat{V}]$$

$\square$

### 4.4 Evaluation

**Lemma 6** Value evaluation $\hat{V}$ is sound, i.e.

$$\eta_{\text{dump}} d = p,
\quad m \in \gamma_{\text{Memory}} \hat{m}
\quad \implies \forall e \in \text{Expr} : \mathcal{V} d \ m \ e \in \gamma_{\text{Val}} (\mathcal{V} \hat{p} \hat{m} \ e)$$

**Proof** $\mathcal{V} d \ (M_{\_\_\_}) = \mathcal{V} d \ M$ and $\mathcal{V} \hat{p} \ (\hat{M}_{\_\_\_}) = \mathcal{V} \hat{p} \hat{M}$. Therefore, let us show $\mathcal{V} d \ M \ e \in \gamma_{\text{Val}} (\mathcal{V} \hat{p} \hat{M} \ e)$ when $M \in \gamma_{\text{Mapp}} \hat{M}$ instead. This is proved by structural induction on $e$.

When $e = n$,

$$\mathcal{V} d \ M \ n = n \in \gamma_{\text{Val}} ([n, n]) = \gamma_{\text{Val}} (\mathcal{V} \hat{p} \hat{M} \ n)$$

When $e = X$,

$$\mathcal{V} d \ M \ X = X \in \gamma_{\text{Val}} \{X\} = \gamma_{\text{Val}} (\mathcal{V} \hat{p} \hat{M} X)$$

When $e = x$, since $\eta_{\text{dump}} d = p$,

$$\mathcal{V} d \ M \ x = (d, x) \in \gamma_{\text{Val}} \{(p, x)\} = \gamma_{\text{Val}} (\mathcal{V} \hat{p} \hat{M} x)$$
When \( e = e_1 \star e_2 \), assuming \( \gamma_{Val}(\hat{V} \hat{p} M e) \) for \( i \in \{1, 2\} \),
\[
\begin{align*}
\gamma_{Val}(\hat{V} d M e_1 \star e_2) & = \gamma_{Val}(\hat{V} d M e_1) \star \gamma_{Val}(\hat{V} p M e_2) \\
& \in \gamma_{Val}(\hat{V} p M e_1 \star \hat{V} p M e_2) \\
& = \gamma_{Val}(\hat{V} p M e_1 \star e_2)
\end{align*}
\]
(definition of \( V \)).

When \( e = e'.f \), assuming \( \gamma_{Val}(\hat{V} d M e') \),
\[
\begin{align*}
\gamma_{Val}(\hat{V} d M e'.f) & = \gamma_{Val}(\hat{V} p M e') \\
& \in \{\gamma_{Val}(\hat{V} p M e')| \hat{V} p M e' \in \hat{A} p M e'\}
\end{align*}
\]
(definition of \( \gamma \)).

When \( e = \$ e' \), assuming \( \gamma_{Val}(\hat{V} d M e') \),
\[
\begin{align*}
\gamma_{Val}(\hat{V} d M \$ e') & = \gamma_{Val}(\hat{M} (\hat{A} d M e')) \\
& \in \{\gamma_{Val}(\hat{M} (\hat{A} p M e'))| \hat{A} p M e' \in \hat{A} p M e'\}
\end{align*}
\]
(definition of \( \gamma \)).

Thus, by structural induction, we have shown \( \hat{V} \) is sound. □

**Corollary 1**  
Address evaluation \( \hat{A} \) is sound, i.e.
\[
\eta_d = p, \quad m \in \gamma_{Memory} \hat{m} \\
\implies \forall e \in \Expr: \hat{A} d m e \in \gamma_{Add} \hat{A} p \hat{m} e
\]

**Proof**  
Since definition of both \( \hat{A}, \hat{A} \) are restrictions of \( V, \hat{V} \) respectively, soundness of \( \hat{A} \) follows from Lemma 6. □

**Lemma 7**  
Binary operations +, -, *, / are sound, i.e.
\[
\star \in \{+, -, *, /\} \\
v \in \gamma_{Val} \hat{V}, v' \in \gamma_{Val} \hat{V}' \\
\implies v \star v' \in \gamma_{Val} (\hat{V} \star \hat{V}')
\]
Proof. Let us consider each binary operator separately.

Addition \( \oplus \) on intervals is sound since
\[
\begin{align*}
\{ x + y | x \in \gamma_Z \{a, b\}, y \in \gamma_Z \{c, d\} \} &= \{ x + y | a \leq x \leq b, c \leq y \leq d \} \\
&= \{ x + y | a + c \leq x + y \leq b + d \} \\
&= \{ x \in \{a + c, b + d\} \}
\end{align*}
\]

\( \ominus \) as a shift operator on indexed addresses is also sound since
\[
\begin{align*}
\gamma_{\text{Add}} (\hat{r}, \hat{r} + n) &= \gamma_{\text{Add}} (\hat{r}, \hat{r} + n) \\
&= (\hat{r}, \hat{r} + n) \in \gamma_{\text{Val}} (\hat{r}, \hat{r} + n) \in \hat{V} \cup \hat{V}' \text{ by definition.}
\end{align*}
\]

Subtraction \( \ominus \) on intervals is sound since
\[
\begin{align*}
\{ x - y | x \in \gamma_Z \{a, b\}, y \in \gamma_Z \{c, d\} \} &= \{ x - y | a \leq x \leq b, c \leq y \leq d \} \\
&= \{ x - y | a - d \leq x - y \leq b - c \} \\
&= \{ x \in \{a - d, b - c\} \}
\end{align*}
\]

\( \oslash \) as a distance operator on indexed addresses is also sound since
\[
\begin{align*}
\gamma_{\text{Add}} (\hat{r}, \hat{r} + n) &= \gamma_{\text{Add}} (\hat{r}, \hat{r} + n) \\
&= (\hat{r}, \hat{r} + n) \in \gamma_{\text{Val}} (\hat{r}, \hat{r} + n) \in \hat{V} \cup \hat{V}' \text{ by definition.}
\end{align*}
\]

Multiplication \( \odot \) on intervals is sound since
\[
\begin{align*}
\{ x \times y | x \in \gamma_Z \{a, b\}, y \in \gamma_Z \{c, d\} \} &= \{ x \times y | a \times X = a \times y \times y \leq d \} \\
&= \{ x \times y | \min X \leq x \times y \leq \max X \} \\
&= \{ x \in \{a \times y, b \times y\} \}
\end{align*}
\]

Division \( \oslash \) on intervals is sound because of the following two arguments. When \( c \leq 0 \leq d, \)
\[
\begin{align*}
\{ x / y | x \in \gamma_Z \{a, b\}, y \in \gamma_Z \{c, d\} \} &= \{ x / y | a \leq x \leq b, c \leq y \leq d \} \\
&= \{ x / y | a \leq x \leq b, c \leq y \leq d \} \\
&= \{ x / y | \min X \leq x / y \leq \max X \} \\
&= \{ x \in \{a / b \times y, b / y \times y\} \}
\end{align*}
\]

Otherwise when \( c \leq d < 0 < c \leq d, \)
\[
\begin{align*}
\{ x / y | x \in \gamma_Z \{a, b\}, y \in \gamma_Z \{c, d\} \} &= \{ x / y | a \leq x \leq b, c \leq y \leq d \} \\
&= \{ x / y | a \leq x \leq b, c \leq y \leq d \} \\
&= \{ x / y | \min X \leq x / y \leq \max X \} \\
&= \{ x \in \{a / b \times y, b / y \times y\} \}
\end{align*}
\]
\begin{equation}
\setlength\arraycolsep{2pt}
\begin{array}{ll}
W & \in \text{Worklist} \\
b, b' & \in \text{Block}_F \\
old, new & \in \hat{\text{Memory}} \\
\hat{\mathcal{G}} & \in \hat{\text{Graph}} \\
\hat{T}_\text{in}, \hat{T}_\text{out} & \in \hat{\text{Table}} \\
\hat{D} & \in \hat{\text{Dump}} \\
\hat{\mathcal{F}}_b & : \hat{\text{Graph}} \times \hat{\text{Memory}} \times \hat{\text{Dump}} \rightarrow \hat{\text{Graph}} \times \hat{\text{Memory}} \times \hat{\text{Dump}}
\end{array}
\end{equation}

\text{input}(\mathcal{W}, (\hat{\mathcal{G}}, \hat{T}_\text{in}, \hat{D}))$

\[ \hat{T}_\text{out} := \bot \text{Table} \]

\text{until (isempty}(\mathcal{W})) :

\[(b, \mathcal{W}) := \text{choose}(\mathcal{W}); \ old := \hat{T}_\text{out}(b)\]

\[(\hat{\mathcal{G}}, \text{new}, \hat{D}) := \hat{\mathcal{F}}_b(\hat{\mathcal{G}}, \hat{T}_\text{in}(b), \hat{D})\]

\text{if (isloophead}(\hat{\mathcal{G}}, b)) \ new := \text{old } \triangledown \text{new} \]

\[\hat{T}_\text{out}(b) := \text{new}\]

\text{foreach } (b, b') \in \hat{\mathcal{G}}: \]

\text{if (new } \not\in \hat{T}_\text{in}(b')) \hat{T}_\text{in}(b') := \text{new}; \ W := \text{add}(W, b')

\text{output}(\hat{\mathcal{G}}, \hat{T}_\text{in}, \hat{D})

Figure 1: Worklist algorithm

5 Implementation

5.1 Worklist algorithm

We use the worklist algorithm shown in Figure 1 to reduce the amount of computations for finding an approximation of the least fixpoint. This algorithm can be understood as a block-scopic, incremental version of $\hat{\mathcal{F}}$ in the definition of the abstract semantics. This algorithm keeps track of state of each block and only computes the one that changes. $\hat{\mathcal{F}}_b$ is the semantic transfer function that computes the effect of a single block $b$. $\hat{T}_\text{in}$ and $\hat{T}_\text{out}$ records the input and output state of each block respectively. $\hat{T}_\text{in}$ is the $\hat{T}$ we have seen in the previous definitions. After the widening iterations, narrowing iterations are done by replacing $\not\in$ for $\subseteq$ and $\triangledown$ for $\forall$.

5.1.1 Wait at join

A worklist managing strategy called wait at join can be used to reduce the amount of computation in many cases. When one manages the worklist in a LIFO(last in first out) manner, the computation is done depth first order. Computations for blocks following a join point are repeated as many as the number of branches in worst cases. The heuristic of this strategy is to wait at such join points rather than continuing computation. The computation suspended on join points and their descendants are resumed after no other place needs to be computed.

To implement this, two sub-worklists are used: active $A$ and waiting $W$. Both are managed in a LIFO manner.

\[(A, W) \in \text{Worklist} = \wp(\text{Block}_F) \times \wp(\text{Block}_F)\]

When choosing blocks, active has priority over waiting.

\text{choose : Worklist } \rightarrow \text{Block}_F \times \text{Worklist}

\[\text{choose } (A, W) = (b, (A - \{b\}, W)) \quad \text{if } b \in A\]

\[\text{choose } (\emptyset, W) = (b, (\emptyset, W - \{b\})) \quad \text{if } b \in W\]
Join blocks which have more than two predecessors are always added to waiting, and others are added to active.

\[ add : \text{Worklist} \times \text{Block}_p \rightarrow \text{Worklist} \]

\[ add \left( (A, W), b \right) = \left( A, W \cup \{ b \} \right) \quad \text{if} \quad (b', b), (b'', b) \in \hat{C}, b' \neq b'' \]

\[ = (A \cup \{ b \}, W) \quad \text{otherwise} \]

### 5.2 Widening/Narrowing

Widening is used for finishing the fixpoint computation in finite steps, and narrowing for recovering some accuracy lost by widening. For intervals, we use the same widening and narrowing defined in [9]. The widening extrapolates unstable bounds to zero or infinity, and the narrowing improves those bounds.

\[ \nabla : \hat{\mathbb{Z}} \times \hat{\mathbb{Z}} \rightarrow \hat{\mathbb{Z}} \]

\[
\begin{align*}
\bot \nabla \hat{n} &= \hat{n} \\
\hat{n} \nabla \bot &= \hat{n} \\
[n, m] \nabla [n', m'] &= [0 \leq n' < n ? 0 : n < n' ? -\infty : n, \\
&\quad \quad m < m' \leq 0 ? 0 : m < m' ? +\infty : m]
\end{align*}
\]

\[ \triangle : \hat{\mathbb{Z}} \times \hat{\mathbb{Z}} \rightarrow \hat{\mathbb{Z}} \]

\[
\begin{align*}
\bot \triangle \hat{n} &= \bot \\
\hat{n} \triangle \bot &= \bot \\
[n, m] \triangle [n', m'] &= [n \leq 0 \leq n' ? n' : n = -\infty ? n' : n, \\
&\quad \quad m' \leq 0 \leq m ? m' : m = +\infty ? m' : m]
\end{align*}
\]

Widening and narrowing on other domains are natural extensions of those on intervals. Widening on finite maps are defined pointwisely. Widening between two indexed addresses Region × Index of same region applies interval widening to the index part. Widening on set of abstract values are defined as applying widening only between interval elements and between indexed addresses. Other values, e.g. variables, field addresses are simply joined since only finite number of them are generated for a single program.

### 5.3 Localizing operators

#### 5.3.1 Motivation

Every abstract memory of each block in our abstract interpretation can contain entries of local variables of other procedures. This not only affects the accuracy of join and widening operators but also increases the cost of those operators. If we assume the number of variables is proportional to the size of the program, the operators on abstract memories have time complexity quadratic to the program size.

Reducing the cost of operations on abstract memory is the key to improving the analysis speed. Each command and block of G form can only change a few number of entries in the memory, so one can expect the operators on abstract memory to have time complexity linear to such small number. Since operations on abstract memory are used multiple times in every step of fixpoint iteration of the worklist algorithm, reducing their cost will speed up each step and make the entire analysis faster.

We want the operators to perform the actual computation only for the locally changed entries. We will call this modification localizing the operators. Let us consider a typical case of join after branches that looks like Figure 2. At the join point, \( \hat{M}_1 \cup \hat{M}_2 \) must be computed. Instead of scanning all the entries of \( \hat{M}_1 \) and \( \hat{M}_2 \), we want the operators to compute the join only for \( \hat{M}_1 \) and \( \hat{M}_2 \), and skip the common parts of \( \hat{M} \). A similar idea to this approach was briefly mentioned in the report of ASTRÉE[3, §6.2].
5.3.2 Observation

Localizing the operators are possible, when one uses idempotent join, widening, narrowing operators on values and pointwise extensions for maps. Our operators $\star \in \{\sqcup, \triangledown, \triangleleft\}$ used in the abstract interpretation suffices such conditions.

$$x \star x = x$$

$$\hat{M} \star \hat{M}'(x) = \hat{M}(x) \star \hat{M}'(x)$$

$$\hat{I} \star \hat{I}'(x) = \hat{I}(x) \star \hat{I}'(x)$$

An operator $\star : Y \times Y \to Y$ is idempotent if and only if $\forall y \in Y : y \star y = y$. Operator $\star : (X \xrightarrow{\text{fin}} Y) \times (X \xrightarrow{\text{fin}} Y) \to (X \xrightarrow{\text{fin}} Y)$ is pointwisely defined if and only if

$$(A \star B)(x) = A(x) \star B(x)$$

where $A, B : X \to Y$ and $\star$ is also defined on $Y$.

**Theorem 2** Idempotent operators extended to finite maps are localizable if defined pointwisely.

$\star : Y \times Y \to Y$ is idempotent,

$\star : (X \xrightarrow{\text{fin}} Y) \times (X \xrightarrow{\text{fin}} Y) \to (X \xrightarrow{\text{fin}} Y)$ is pointwisely defined,

$A, B, C : X \xrightarrow{\text{fin}} Y$,

$\text{dom}(A) \cap \text{dom}(B) = \emptyset, \text{dom}(A) \cap \text{dom}(C) = \emptyset$

$\implies (AB) \star (AC) = A(B \star C)$

**Proof** For all $x \in X$, if $x \in \text{dom}(A)$, then

$$(AB)(x) \star (AC)(x) = (AB)(x) \star (AC)(x) \quad \text{(pointwise definition of $\star$)}$$

$$= A(x) \star A(x) \quad \text{(domain are disjoint)}$$

$$= A(x) \quad \text{(idempotence of $\star$)}.$$

Otherwise,

$$(AB)(x) \star (AC)(x) = (AB)(x) \star (AC)(x) \quad \text{(pointwise definition of $\star$)}$$

$$= B(x) \star C(x) \quad \text{(domain are disjoint)}$$

$$= (B \star C)(x) \quad \text{(pointwise definition of $\star$)}.$$

Hence, we have

$$(AB)(x) \star (AC)(x) = A(x) \quad \text{if } x \in \text{dom}(A)$$

$$\quad = (B \star C)(x) \quad \text{otherwise}$$
whose right hand side can be rewritten as

\[(B \ast C)A)(x).\]

Since domains are disjoint, it is equal to

\[(A(B \ast C))(x).\]

Thus, we have shown

\[(AB) \ast (AC) = A(B \ast C)\]

\[\square\]

5.3.3 Journaling memory

In order to keep track of changes, we attach a journal to the abstract memory. Let us call this journaling memory. A journaling memory \(j \in JMem\) is either 1) \(\diamond \hat{M}\) with the empty journal \(\diamond\), or 2) \(\langle j \rangle \hat{M}\) with a journal \(\langle j \rangle\) that means \(\hat{M}\) was derived by modifying the entries of \(\delta\) from \(j\).

\[
\begin{align*}
\hat{M} & \in \hat{Map} \\
\hat{M} & :: = j \\
\langle j \rangle & :: = \diamond \\
\langle j \rangle & :: = \diamond \langle \hat{J} \rangle \\
\langle \hat{J} \rangle & :: = \langle \hat{J} \rangle \\
\end{align*}
\]

where \(\hat{M} \in \hat{Map}\) and \(\delta \subseteq \hat{Addr}\).

Proxy operators

The lookup and update operations on \(JMem\) are exactly the same ones on the outermost \(\hat{Map}\) part. The only difference is that update operator adds the address to the outermost journal if it exists. Since these operators are the only ones used in the semantics definition of \(c\) and \(e\), and they remain exactly the same as before, none of the semantics part needs to be modified.

\[
\begin{align*}
\cdot(\cdot) & : JMem \rightarrow \hat{Addr} \rightarrow Val \\
(j \hat{M})(a) & = \hat{M}(a) \\
\cdot\langle \cdot \rangle \rightarrow \cdot \rangle & : JMem \rightarrow \hat{Addr} \rightarrow \hat{Val} \rightarrow JMem \\
\langle \diamond \hat{M} \rangle(a \mapsto v) & = \diamond \hat{M}(a \mapsto v) \\
\langle \langle \langle \hat{J} \rangle \rangle \rangle(a \mapsto v) & = \langle \langle \hat{J} \rangle \rangle(a \mapsto v) \\
\end{align*}
\]

Controlling depth of journal

Operators \(wrap\) and \(peel\) controls the level of journals. \(wrap\) records the current journaling memory by wrapping it with a new journal,

\[
\begin{align*}
\text{wrap} & : JMem \rightarrow JMem \\
\text{wrap}(j) & = \langle j \rangle \hat{M} \text{ where } j \hat{M} = J
\end{align*}
\]

and \(peel\) peels off the outermost journal and merges the changes into the journal inside it.

\[
\begin{align*}
\text{peel} & : JMem \rightarrow JMem \\
\text{peel}(\diamond \hat{M}) & = \diamond \hat{M} \\
\text{peel}(\langle \langle \hat{J} \rangle \rangle \hat{M}) & = \langle \hat{J} \rangle \hat{M}
\end{align*}
\]

Both do not touch any entries in the outermost \(\hat{Map}\) part.

Depth of journals

The number of nested journals \(n\) of \(j \in JMem\) are denoted \(j^n\). It is defined inductively as following.

\[
\begin{align*}
(\diamond \hat{M}) & \rightarrow \hat{M} \\
(\langle \hat{J} \rangle) & \rightarrow \hat{M}^{n+1}
\end{align*}
\]
Difference and common journal  

Difference and common part between two JMem needs to be known to localize the operators. ⊖ gathers the addresses of modified entries in two maps.

\[
\text{⊖} : \text{JMem} \to \text{JMem} \to \wp(\text{Addr})
\]

\[
\begin{align*}
(\diamond \hat{M}_1) & \ominus (\diamond \hat{M}_2) = \text{dom}(\hat{M}_1) \cup \text{dom}(\hat{M}_2) \\
((\hat{L})_{\delta}, M_1) & \ominus (\diamond \hat{M}_2) = \text{dom}(\hat{M}_1) \cup \text{dom}(\hat{M}_2) \\
(\diamond M_1) & \ominus (\hat{L})_{\delta, \phi} \hat{M}_2 = \text{dom}(\hat{M}_1) \cup \text{dom}(\hat{M}_2) \\
((\hat{L})_{\delta}, M_1) & \ominus (\hat{L})_{\gamma} \hat{M}_2 = \delta_1 \cup \delta_2 \\
((\hat{L})_{\delta}, M_1) & \ominus (\hat{L})_\delta = \delta_1 \\
(\hat{L}) & \ominus (\hat{L})_{\gamma} \hat{M}_2 = \delta_2 \\
(j^p_1) & \ominus (j^p_2) = \begin{cases} 
(\text{peel}(j_1)) \ominus (\hat{L})_\delta & \text{if } n_1 > n_2 \\
(\hat{L})_\delta \ominus (\text{peel}(j_2)) & \text{otherwise}
\end{cases}
\]

\text{⊖} computes the common journal that can be used after merging two maps.

\[
\text{⊕} : \text{JMem} \to \text{JMem} \to \text{Jnl}
\]

\[
\begin{align*}
(\diamond M_1) & \oplus (\diamond M_2) = \diamond \\
((\hat{L})_{\delta}, M_1) & \oplus (\diamond M_2) = \diamond \\
(\diamond M_1) & \oplus (\hat{L})_{\delta, \phi} M_2 = \diamond \\
((\hat{L})_{\delta}, M_1) & \oplus (\hat{L})_\delta = \hat{L}_\delta \\
((\hat{L})_{\delta}, M_1) & \oplus (\hat{L})_\delta = \hat{L}_\delta \\
(\hat{L}) & \oplus (\hat{L})_{\gamma} \hat{M}_2 = \hat{L}_\delta \\
(j^p_1) & \oplus (j^p_2) = \begin{cases} 
(\text{peel}(j_1)) \oplus (\hat{L})_\delta & \text{if } n_1 > n_2 \\
(\hat{L})_\delta \oplus (\text{peel}(j_2)) & \text{otherwise}
\end{cases}
\]

Selective operators  

Finally, let us define the selective operators \( \bigstar \in \{\ominus, \lor, \odot, \ominus\} \) as following.

\[
\bigstar : \text{JMem} \to \text{JMem} \to \text{JMem}
\]

\[
(j_1 \hat{M}_1, j_2 \hat{M}_2) = j (\hat{M}_1 \bigstar j_2 \hat{M}_2)
\]

where \( \delta = (j_1 \hat{M}_1) \ominus (j_2 \hat{M}_2) \)

and \( j = \begin{cases} 
(\hat{L})_{\gamma} \ominus & \text{if } (\hat{L})_{\gamma} = (j_1 \hat{M}_1) \ominus (j_2 \hat{M}_2) \\
\emptyset & \text{otherwise}
\end{cases} \)

\( \bigstar \) is the corresponding operator on Map that does the actual computation only for the entries of \( \delta \).

5.3.4 Localizing operators

Journaling rules  

If we apply wrap appropriately, the selective operators can operate only on local changes. The heuristic is to apply wrap on maps that might join later with slight differences, so the unmodified common part can be skipped later. Two following wrapping rules handle this.

1. \text{wrap} output of nodes that have more than two successors.
2. \text{wrap} output of loop heads.

The first rule is because the map propagated to branches will probably join later. The second is because output of the loop head will eventually join at itself. These wrapping can be done at the worklist algorithm after computing the effect of a single block with \( \tilde{f}_b \). This is the only modification needs to be made for the analyzer.
Examples  Let us look at some examples. Figure 3 shows the branch example with localized operators. Comparing it to the case with normal operators in Figure 2, one can see the cost of join has been reduced to the number of local changes in $\Delta_1$ or $\Delta_2$. Figure 4 shows a loop without localized operations. Figure 5 illustrates each join cost after an iteration of the loop body is reduced to size of local changes. Although it is not shown in the figure, the cost of widening operation also applied on output of each loop head is also reduced. Section 6.3.2 shows the experiment result measuring the speed up of this technique in the array index range analyzer, Airac5.

$$\tilde{j} = j \tilde{M}$$

$$(\tilde{j})_{\delta_1} \tilde{M}_{\Delta_1}$$

$$(\tilde{j})_{\delta_2} \tilde{M}_{\Delta_2}$$

$$(\tilde{j})_{\delta_1 \cup \delta_2} (\tilde{M}_{\Delta_1} \sqcup \tilde{M}_{\Delta_2})$$

Figure 3: The branch with localized join

$$\tilde{M}$$

$$(\sqcup_{\text{ench}} \tilde{M}(\Delta_1 \Delta_2)^{i+1})$$

$$(\sqcup_{\text{ench}} \tilde{M}(\Delta_1 \Delta_2)^{i+1})$$

$$(\sqcup_{\text{ench}} \tilde{M}(\Delta_1 \Delta_2)^{i}) \Delta_1$$

$$(\sqcup_{\text{ench}} \tilde{M}(\Delta_1 \Delta_2)^{i}) \Delta_1$$

Figure 4: A loop without localized join

Another view  One may achieve the same goal by collecting changes made in the path from the immediate dominator[1, §10.4] to the join point. However, traditional methods for computing dominators is not trivial to extend to work efficiently for interprocedural graphs which can change during the analysis. Moreover, we believe mentioning the graph structure in the definition of operators of maps increases the complexity to understand for readers. Our aim was to localize the operators independently of other parts of the analyzer. Our technique of localizing operators are done by keeping track of changes in them and few modifications were made to other parts of the analyzer.
6 Application: Array Index Range Analysis

6.1 Buffer overrun error

Buffer overrun is a root cause of a major number of security breaches and malfunctions of software. Intruder manipulates the stack of privileged processes using buffer overrun errors and executes programs they want with the same privileges. Accessing area out of allocated boundaries may cause the program to stop, or even continue with totally unknown value which leads to undefined and mysterious behaviors. Some programming environments use runtime checks around every memory access to prevent this error. It can prevent the program from having undefined behavior, but does not fix the program from stopping. Besides, they have great impact on the performance of the program.

We present static analysis of array index ranges using our abstract interpretation here. By conservatively analyzing array index ranges and bounds, all buffer overrun errors can be detected. The implementation called Airac5[18] is able to analyze real C programs. It found number of real bugs in the Linux Kernel and some GNU software with reasonable cost. The analysis results of some free and open source software are shown in Section 6.3.1. Although our goal is to guide the programmer to fix the error and make programs more robust, the analysis results can also be used for removing runtime checks in the target program to improve its running performance.

Huge amount of work is done on detecting buffer overrun or overflow errors. There is an extensive survey[16] on causes of buffer overrun error and its solutions. Some[13, 10] suggest methods inserting run-time bound checks to detect buffer overruns, while some others[5, 20] try to eliminate those inserted checks that are safe to remove, to reduce useless computation. Others[15, 21, 19, 11] present static analyses that detect buffer overrun errors at compile-time, so that programmers can fix them.

6.2 Array index range analysis

For a G program P, buffer overrun errors can be found from an approximation ((Î, Ô, Ô) ∈ Summary of the least fixpoint of Ï P. Bad addresses can be found by checking all the indexes of addresses used for memory access against the size of the region. We can find program codes that are using such addresses by simply running the abstract interpretation once more with the computed fixpoint, and gather used addresses with allocation information for inspection.

Let us formalize this inspection process.

We define the set of buffer overrun errors found from a fixpoint ((Î, Ô, Ô) ∈ Summary as

\{(Î, Ô) ∈ I_b \cdot b \cdot \hat{m} \mid b \cdot \hat{m} \hat{\in} T, \text{Overrun}(Î, Ô)\}. 

Figure 5: A Loop with localized join
Overrun(\(\hat{r}, \hat{i}, I\)) denotes the fact that an address \((\hat{r}, \hat{i}) \in \text{Region} \times \text{Index}\) used under some allocation context \(I\) can cause overrun because

\[ [\hat{i}] \cap S_{=\text{}} \neq \bot_{\text{value}} \quad \text{or} \quad [\hat{i}] \cap S_{<} ([0,0]) \neq \bot_{\text{value}} \]

where \(\hat{r} \mapsto (s_{\_}, e_{\_}) \in I\).

\(I_{b}, I_{c}, I_{\_}\) gathers the set in \(\varphi(\text{AddrUsed})\) containing all address \(A\) used for updating or looking up the abstract memory during the interpretation of block, command, and expression respectively. This is exactly the same set containing all the address \(A\) appeared as \(M[A \mapsto \_]\) or \(M \uparrow A\) in the definition of \(\hat{F}\) or \(\hat{R}, \hat{V}\). \(\text{AddrUsed}\) has address with the information when it was used.

\[
\text{AddrUsed} = \text{Addr} \times \text{Alloc}
\]

\[
I_{b} : \quad \text{Block}_{p} \rightarrow \text{Dump} \rightarrow \text{Memory} \rightarrow \varphi(\text{AddrUsed})
\]

\[
I_{b} \uparrow^{p} \hat{D}(\hat{M}, \hat{i}) = \bigcup \{ I_{b}(e, p)(\hat{M}, \hat{i}) \cup I_{b}(c_{\_}, p)(\hat{M}, \hat{i}) \cup \{[(p, p'), \hat{M} x_{\hat{i}}]\} | \quad \hat{p} = \text{CALL}(e_{\_}, c_{\_}, c_{\_}) \}
\]

\[
I_{c} : \quad \text{Cmd} \times \text{Procl} \rightarrow \text{Dump} \rightarrow \text{Memory} \rightarrow \varphi(\text{AddrUsed})
\]

\[
I_{c}\ (\text{SET}(c_{\_}, e_{\_}), p) \hat{D}(\hat{M}, \hat{i}) = I_{c}(c_{\_}, p)(\hat{M}, \hat{i}) \cup I_{c}(e_{\_}, p)(\hat{M}, \hat{i})
\]

\[
\text{∪ } \{[(\hat{p} \hat{M} e_{\hat{i}}), \hat{i}]\}
\]

\[
I_{c}\ (\text{ESCAPE}(e_{\_}), p) \hat{D}(\hat{M}, \hat{i}) = I_{c}(e_{\_}, p)(\hat{M}, \hat{i}) \cup \{[(\hat{D} p, \hat{i})]\}
\]

\[
I_{c}\ (\text{ASSUME}(e \mapsto e'), p) \hat{D}(\hat{M}, \hat{i}) = I_{c}(e, p)(\hat{M}, \hat{i}) \cup I_{c}(e', p)(\hat{M}, \hat{i})
\]

\[
I_{c}\ (\text{ALLOC}(c_{\_}, [e]), p) \hat{D}(\hat{M}, \hat{i}) = I_{c}(c_{\_}, p)(\hat{M}, \hat{i})
\]

\[
I_{c}\ (\text{ALLOC}(c_{\_}, [e]), p) \hat{D}(\hat{M}, \hat{i}) = I_{c}(c_{\_}, p)(\hat{M}, \hat{i}) \cup I_{c}(e, p)(\hat{M}, \hat{i})
\]

\[
I_{c}\ (\text{FREE}(c), p) \hat{D}(\hat{M}, \hat{i}) = I_{c}(c, p)(\hat{M}, \hat{i})
\]

\[
I_{\_} : \quad \text{Expr} \times \text{Procl} \rightarrow \text{Memory} \rightarrow \varphi(\text{AddrUsed})
\]

\[
I_{\_}(e \star e', p)(\hat{M}, \hat{i}) = I_{\_}(e, p)(\hat{M}, \hat{i}) \cup I_{\_}(e', p)(\hat{M}, \hat{i})
\]

\[
I_{\_}(e, I, p)(\hat{M}, \hat{i}) = I_{\_}(e, p)(\hat{M}, \hat{i})
\]

\[
I_{\_}(\text{SC}, p)(\hat{M}, \hat{i}) = I_{\_}(e, p)(\hat{M}, \hat{i}) \cup \{[(\hat{A} p \hat{M} c, \hat{i})]\}
\]

\[
I_{\_}(\_, p)(\hat{M}, \hat{i}) = \emptyset
\]

### 6.3 Implementation: Airac5

We have implemented an abstract interpreter that can analyze real C programs. It is called Airac5[18]. First, it transforms programs written in ANSI C with some GNU extensions into Q programs. Then it computes an approximation of the least fixpoint which is then inspected to find buffer overrun alarms. Airac5 uses all the implementation techniques described in Chapter 5.

#### 6.3.1 Performance

We have analyzed some small device drivers and modules of Linux Kernel 2.6.4, as well as large programs of GNU. Table 1 and 2 shows analysis speed and accuracy data of Airac5. All analysis was done in a Linux 2.6 system running on a Pentium 4 3.2GHz box with 4GB of main memory. We lined every procedure one depth from the root of the call tree. Procedures in each file were analyzed separately ignoring the side effects of procedures in other files. LOC are the number of line of the C source codes, before preprocessing them. Time are the CPU times spent during analysis. #Alarms are the number of alarms Airac5 raised, and #Bugs are the number of real bugs we have confirmed in that program.

In order to effectively analyze real world C programs, we used some assumptions that may affect the soundness of our analysis. We assumed free variables can have any value,
Table 1: Airac5’s performance on Linux Kernel 2.6.4

<table>
<thead>
<tr>
<th>Filename</th>
<th>LOC</th>
<th>Time(sec)</th>
<th>#Alarms</th>
<th>#Bugs</th>
</tr>
</thead>
<tbody>
<tr>
<td>vmax302.c</td>
<td>246</td>
<td>161.38</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>cdc-acm.c</td>
<td>849</td>
<td>199.15</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>atkbd.c</td>
<td>944</td>
<td>526.75</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>eata_pio.c</td>
<td>984</td>
<td>2319.11</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>ip6_output.c</td>
<td>1110</td>
<td>7857.39</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>xfrm_user.c</td>
<td>1201</td>
<td>5974.44</td>
<td>9</td>
<td>1</td>
</tr>
<tr>
<td>keyboard.c</td>
<td>1256</td>
<td>1089.25</td>
<td>18</td>
<td>1</td>
</tr>
<tr>
<td>af_inet.c</td>
<td>1273</td>
<td>9825.47</td>
<td>25</td>
<td>2</td>
</tr>
<tr>
<td>usb-midi.c</td>
<td>2206</td>
<td>2784.22</td>
<td>4</td>
<td>2</td>
</tr>
<tr>
<td>aty128fb.c</td>
<td>2466</td>
<td>972.09</td>
<td>2</td>
<td>1</td>
</tr>
<tr>
<td>keyboard.c</td>
<td>1256</td>
<td>1089.25</td>
<td>18</td>
<td>1</td>
</tr>
</tbody>
</table>

Table 2: Airac5’s performance on free or open source software

<table>
<thead>
<tr>
<th>Program</th>
<th>LOC</th>
<th>Time(sec)</th>
<th>#Alarms</th>
<th>#Bugs</th>
</tr>
</thead>
<tbody>
<tr>
<td>tar-1.13</td>
<td>20258</td>
<td>24783.40</td>
<td>424</td>
<td>1</td>
</tr>
<tr>
<td>bison-1.875</td>
<td>25907</td>
<td>20340.19</td>
<td>323</td>
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<tr>
<td>gzip-1.2.4a</td>
<td>7327</td>
<td>18401.30</td>
<td>374</td>
<td></td>
</tr>
<tr>
<td>sed-4.0.8</td>
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<td>51516.45</td>
<td>463</td>
<td></td>
</tr>
<tr>
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<td>33325.10</td>
<td>275</td>
<td></td>
</tr>
<tr>
<td>Doom</td>
<td>471.09</td>
<td>413</td>
<td>413</td>
<td>1</td>
</tr>
</tbody>
</table>

and procedures without source codes have no side effect. For large programs, we analyzed procedures in each file separately, not considering the side effects of procedures in other files. We do not raise alarms for memory access expressions when the size of the target region is not known since they are usually noises due to our approximation. Although there is no semantics defined after erroneous commands, we continued the analysis beyond those points in order to gather all possible errors. We ignore type castings and assume every memory regions are used as the same type they were allocated. Very limited number of library functions that have side effects on memory were considered during the analysis.

6.3.2 Speed up of localizing operators

We implemented the technique for localizing operators suggested in Section 5.3 and measured its speed up. Table 3 shows the speed up of some programs in Linux Kernel 2.6.4. All the experiments were performed also on a Pentium4 3.2GHz, 4GB main memory system. Analyses using localized operators were almost always faster, and sometimes it was nearly twice as faster than using normal operators.

References


Table 3: Speed up of localizing operators

<table>
<thead>
<tr>
<th>Name</th>
<th>Without (s)</th>
<th>With (s)</th>
<th>Improvement</th>
</tr>
</thead>
<tbody>
<tr>
<td>usb-serial.i</td>
<td>548.79</td>
<td>283.85</td>
<td>48.27%</td>
</tr>
<tr>
<td>usb-midi.i</td>
<td>3353.51</td>
<td>1841.59</td>
<td>45.08%</td>
</tr>
<tr>
<td>wavelan.i</td>
<td>10165.58</td>
<td>5741.34</td>
<td>43.52%</td>
</tr>
<tr>
<td>skge.i</td>
<td>49749.93</td>
<td>34551.73</td>
<td>30.54%</td>
</tr>
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<td>19962.56</td>
<td>15711.49</td>
<td>21.29%</td>
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<td>172.66</td>
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<td>16.47%</td>
</tr>
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<td>12.45%</td>
</tr>
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<td>13301.8</td>
<td>8.61%</td>
</tr>
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<td>5154.56</td>
<td>3.40%</td>
</tr>
<tr>
<td>i2o_proc.i</td>
<td>33556.94</td>
<td>35543.75</td>
<td>-5.92%</td>
</tr>
</tbody>
</table>

Average: 15.62%
Standard Deviation: 16.01%


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A Notation

A.1 Functions, Fixpoints

A.1.1 Composition

\[ \circ : (A \to B) \to (C \to A) \to (C \to B) \]

\[ (f \circ g) x = f (g x) \]

\[ ; : (A \to B) \to (B \to C) \to (A \to C) \]

\[ (f ; g) x = g (f x) \]

A.1.2 Fixpoint

For a monotone function \( f : A \to A \) on a complete partially ordered set \( A \), let us use \( \text{lfp}_f \) to
denote its least fixpoint greater than or equal to \( x \in A \).

\[ \text{lfp}_f f = f (\text{lfp}_f f) \quad x \sqsubseteq \text{lfp}_f f \]

\[ \forall a \in A : a = f a, x \sqsubseteq a \implies \text{lfp}_f f \sqsubseteq a \]

A.2 Product and Sum

A.2.1 Product

\( A \times B \) is a Cartesian product of sets \( A \) and \( B \). \((a, b) \in A \times B \) if and only if \( a \in A \) and \( b \in B \).
One may naturally extend this binary product to product of finite number of sets. Below,
\((a_1, \cdots, a_n) \in A_1 \times \cdots \times A_n\).
Projection

\[(a_1, \cdots, a_n) = a_i\]

Insertion

\[a :: \epsilon = (a)\]
\[a :: (a_1, \cdots, a_n) = (a, a_1, \cdots, a_n)\]

A.2.2 Sum

\(A + B\) is a disjunctive sum of sets \(A\) and \(B\). \(c_A \in A + B\) if and only if \(c \in A\) and \(c_B \in A + B\) if \(c \in B\). We omit the subscript \(_A\) or \(_B\) when it is clear where the element belongs to.

A.3 Finite maps

Let us write \(A \to B\) to denote set of finite number of mappings from \(A\) to \(B\). A mapping from \(A\) to \(B\) is a pair of an element in \(A\) and one in \(B\), written \(a \mapsto b\) where \(a \in A, b \in B\), and a finite map is a set of such mappings. There are several operators defined over finite maps. Let \(M\) and \(N\) be of type \(A \to B\).

\[
\text{dom}(\cdot) : (A \to B) \to \wp(A) \quad \text{dom}(M) = \{a \in A | a \mapsto b \in M\}
\]

\[
\cdot \cdot : (A \to B) \times \wp(A) \to (A \to B) \quad M - A' = \{a \mapsto b \in M | a \not\in A'\}
\]

\[
\cdot \cdot : (A \to B) \times (A \to B) \to (A \to B) \quad MN = (M - \text{dom}(N)) \cup N
\]

\[
\cdot(\cdot) : (A \to B) \times A \to B \quad M(a) = b \text{ where } a \mapsto b \in M
\]